

# Cinterion<sup>®</sup> Java Terminal

Hardware Interface Description

Version: 04  
DocId: PLS62T-W\_HID\_v04



Document Name:	<b>Cinterion® Java Terminal Hardware Interface Description</b>
Version:	<b>04</b>
Date:	<b>2020-05-29</b>
DocId:	<b>PLS62T-W_HID_v04</b>
Status	<b>Public / Released</b>

**GENERAL NOTE**

THIS DOCUMENT CONTAINS INFORMATION ON THALES DIS AIS DEUTSCHLAND GMBH ("THALES") PRODUCTS. THALES RESERVES THE RIGHT TO MAKE CHANGES TO THE PRODUCTS DESCRIBED HEREIN. THE SPECIFICATIONS IN THIS DOCUMENT ARE SUBJECT TO CHANGE AT THE DISCRETION OF THALES. THE PRODUCT AND THIS DOCUMENT ARE PROVIDED ON AN "AS IS" BASIS ONLY AND MAY CONTAIN DEFICIENCIES OR INADEQUACIES. THALES DOES NOT ASSUME ANY LIABILITY FOR INFORMATION PROVIDED IN THE DOCUMENT OR ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT DESCRIBED HEREIN.

THALES GRANTS A NON-EXCLUSIVE RIGHT TO USE THE DOCUMENT. THE RECIPIENT SHALL NOT COPY, MODIFY, DISCLOSE OR REPRODUCE THE DOCUMENT EXCEPT AS SPECIFICALLY AUTHORIZED BY THALES.

Copyright © 2020, THALES DIS AIS Deutschland GmbH

**Trademark Notice**

Thales, the Thales logo, are trademarks and service marks of Thales and are registered in certain countries. Microsoft and Windows are either registered trademarks or trademarks of Microsoft Corporation in the United States and/or other countries. All other registered trademarks or trademarks mentioned in this document are property of their respective owners.

## Contents

<b>0</b>	<b>Document History</b> .....	<b>8</b>
<b>1</b>	<b>Introduction</b> .....	<b>9</b>
1.1	Related Documents .....	10
1.2	Terms and Abbreviations .....	10
1.3	Regulatory and Type Approval Information .....	12
1.3.1	Directives and Standards .....	12
1.3.2	Safety Precautions .....	15
1.4	Product Label .....	17
<b>2</b>	<b>Product Concept</b> .....	<b>18</b>
2.1	Key Features at a Glance .....	18
<b>3</b>	<b>Interface Description</b> .....	<b>21</b>
3.1	Overview .....	21
3.2	Block Diagram .....	22
3.3	Terminal Circuit .....	23
3.4	Operating Modes .....	24
3.5	RS-232 Interface .....	25
3.5.1	9-Pole D-sub Connector .....	25
3.6	USB Interface (PLS62T-W USB) .....	26
3.7	Ethernet Interface (PLS62T-W LAN) .....	27
3.8	GPIO Interface .....	28
3.8.1	Serial Interface ASC1 .....	30
3.8.2	I <sup>2</sup> C Interface .....	30
3.8.3	SPI Interface .....	31
3.8.4	PWM Interface .....	32
3.8.5	Pulse Counter .....	32
3.8.6	DSR0 signal .....	32
3.9	Power Supply .....	33
3.10	Power Up/Power Down Scenarios .....	34
3.10.1	Turn Java Terminal on .....	34
3.10.2	Reset/Restart Java Terminal .....	34
3.10.3	Turn Java Terminal off .....	35
3.10.4	Disconnecting power supply .....	35
3.11	Automatic thermal shutdown .....	36
3.12	Hardware Watchdog .....	36
3.13	RTC .....	36
3.14	SIM Interface .....	37
3.15	Status LEDs .....	38
3.16	RF Antenna Interface .....	39

---

<b>4</b>	<b>Electrical and Environmental Characteristics</b> .....	<b>40</b>
4.1	Absolute Maximum Ratings .....	40
4.2	Electrical Specifications of the Application Interface.....	41
4.2.1	On/Off Control.....	41
4.2.2	RS-232 Interface.....	41
4.2.3	USB Interface (PLS62T-W USB) .....	41
4.2.4	GPIO Interface .....	42
4.2.5	Ethernet Interface (PLS62T-W LAN) .....	43
4.3	Power Supply Ratings.....	44
4.4	Operating Temperatures.....	48
4.5	Antenna Interface.....	49
4.6	Storage Conditions .....	51
<b>5</b>	<b>Mechanics, Mounting and Packaging</b> .....	<b>52</b>
5.1	Mechanical Dimensions .....	52
5.2	Mounting the Java Terminal.....	54
5.3	Packaging .....	55
<b>6</b>	<b>Full Type Approval</b> .....	<b>56</b>
6.1	Thales Reference Setup .....	56
6.2	Restrictions .....	57
6.3	CE Conformity.....	57
6.4	EMC.....	57
6.5	Compliance with FCC and ISED Rules and Regulations.....	58
<b>7</b>	<b>List of Parts and Accessories</b> .....	<b>60</b>
<b>8</b>	<b>Appendix A: (Hardware) Watchdog</b> .....	<b>62</b>
8.1	Reset Conditions.....	63
8.1.1	Reset stages .....	63
8.1.2	Reset Delay .....	64
8.2	Restart Conditions .....	64
8.3	Complete Watchdog Configuration .....	65
8.3.1	Watchdog (WD) Command Specification .....	65
8.4	Qualified Watchdog Configuration .....	75
8.4.1	I <sup>2</sup> C Interface Command Specification .....	75
8.5	Configuring GPIO Directions.....	80
8.5.1	Configuration using I <sup>2</sup> C Interface .....	81
8.5.2	Configuration using TXD0 Line of ASC0 Interface.....	82

---

<b>9</b>	<b>Appendix B: Ethernet Setup and Usage .....</b>	<b>83</b>
9.1	Connection Setup .....	83
9.2	Connection Usage .....	86
9.2.1	Access LTE Terminal via SSH.....	86
9.2.2	Access LTE Terminal via FTP .....	87
9.2.3	Security Notes.....	88
9.2.4	Configure IP Address.....	88
9.2.5	Edit APN List.....	88
9.2.6	Enter SIM Pin.....	88
9.2.7	Transparent Data Exchange .....	89
9.2.8	Update LTE Module Firmware .....	90
9.2.9	Update Ethernet Sub-System Firmware .....	90
9.2.10	Update Ethernet Sub-System Firmware without Password .....	90

## Tables

Table 1:	Cinterion® Java Terminal overview .....	9
Table 2:	Terms and abbreviations.....	10
Table 3:	Directives .....	12
Table 4:	Standards of North American type approval .....	12
Table 5:	Standards of European type approval.....	12
Table 6:	Requirements of quality .....	13
Table 7:	Standards of the Ministry of Information Industry of the People's Republic of China.....	14
Table 8:	Toxic or hazardous substances or elements with defined concentration limits .....	14
Table 9:	Java Terminal label information .....	17
Table 10:	Java Terminal' interfaces .....	21
Table 11:	Overview of operating modes .....	24
Table 12:	9-pole D-sub (female) RS-232 .....	25
Table 13:	Female 8-pole RJ-45 Ethernet connector .....	27
Table 14:	GPIO connector pin availability and alternate pin functionalities .....	29
Table 15:	Female 6-pole Western plug for power supply, ignition, power down.....	33
Table 16:	Allowed maximum antenna gain (including cable loss).....	39
Table 17:	Absolute maximum ratings.....	40
Table 18:	Operating supply voltage for Java Terminal.....	40
Table 19:	On/Off control line specifications.....	41
Table 20:	RS-232 interface specifications.....	41
Table 21:	GPIO interface specifications (requirements) .....	42
Table 22:	Power supply specifications .....	44
Table 23:	Temperature characteristics.....	48
Table 24:	RF Antenna interface GSM / UMTS / LTE .....	49
Table 25:	Storage conditions .....	51
Table 26:	List of parts and accessories.....	60
Table 27:	Address register for I <sup>2</sup> C commands .....	76
Table 28:	I <sup>2</sup> C status result codes .....	77

## Figures

Figure 1:	Sample Java Terminal label PLS62T-W USB.....	17
Figure 2:	Java Terminal 3D view.....	21
Figure 3:	Block diagram .....	22
Figure 4:	Java Terminal circuit block diagram.....	23
Figure 5:	Pin assignment RS-232 (D-sub 9-pole female).....	25
Figure 6:	USB Type B receptacle interface .....	26
Figure 7:	8-pole RJ-45 Ethernet connector (female).....	27
Figure 8:	PLS62T-W USB: GPIO connectors (8-pin and 12-pin) .....	28
Figure 9:	Characteristics of SPI modes.....	31
Figure 10:	6-pole Western jack for power supply, ignition, reset, typical connection .....	33
Figure 11:	SIM interface .....	37
Figure 12:	Status LED .....	38
Figure 13:	Antenna connectors .....	39
Figure 14:	Java Terminal 3D overview.....	52
Figure 15:	Java Terminal mechanical dimensions .....	53
Figure 16:	Mounting the Java Terminal.....	54
Figure 17:	Reference equipment for approval.....	56
Figure 18:	Hardware watchdog configuration.....	62
Figure 19:	Write data to address register.....	78
Figure 20:	Read data from address register.....	79
Figure 21:	Setting GPIO direction to OUTPUT via I <sup>2</sup> C interface .....	81
Figure 22:	Setting GPIO direction to INPUT via I <sup>2</sup> C interface .....	81
Figure 23:	Setting GPIO direction to OUTPUT via TXD0 line (at 1200bps).....	82
Figure 24:	Setting GPIO direction to INPUT via TXD0 line (at 1200bps).....	82
Figure 25:	PuTTY Configuration Window.....	83
Figure 26:	PuTTY Security Alert Window.....	84
Figure 27:	Packaging Label of PLS62T-W LAN .....	84
Figure 28:	Login via PuTTY.....	84
Figure 29:	Logged in to PLS62T-W LAN.....	85
Figure 30:	Firmware Update Window.....	91
Figure 31:	Firmware for update selected.....	91
Figure 32:	Firmware Update in progress.....	92
Figure 33:	Firmware update done .....	92

## 0 Document History

Preceding document: "Cinterion® Java Terminal Hardware Interface Description" Version 03

New document: "Cinterion® Java Terminal Hardware Interface Description" Version **04**

Chapter	What is new
--	Layout update
<a href="#">3.16</a>	Revised <a href="#">Table 16</a> maximum allowed antenna gain
<a href="#">5.1</a>	Removed Figure 16.
<a href="#">6.5</a>	Added Thales contact person for FCC related compliance information.

Preceding document: "Cinterion® Java Terminal Hardware Interface Description" Version 02

New document: "Cinterion® Java Terminal Hardware Interface Description" Version 03

Chapter	What is new
<a href="#">3.1</a> , <a href="#">3.16</a> , <a href="#">5.1</a> , <a href="#">5.2</a>	Removed 3rd antenna interface <a href="#">Figure 2</a> and <a href="#">Table 10</a> , <a href="#">Figure 13</a> , <a href="#">Figure 15</a> , <a href="#">Figure 5.2</a> , <a href="#">Figure 16</a>
<a href="#">1.3.1</a>	Revised Approval Standards in <a href="#">Table 5</a>
<a href="#">3.6</a>	Added <a href="#">Figure 6</a> USB Type B receptacle interface
<a href="#">7</a>	Revised antenna,power supply and added ordering numbers in <a href="#">Table 26</a>
<a href="#">9.1</a>	Revised connection setup
<a href="#">9.2.9</a>	Revised chapter
<a href="#">9.2.10</a>	Added chapter for resetting password and firmware update

Preceding document: "Cinterion® Java Terminal Hardware Interface Description" Version 01

New document: "Cinterion® Java Terminal Hardware Interface Description" Version 02

Chapter	What is new
<a href="#">3.7</a>	Added remark on supported types of power supplies.
<a href="#">4.1</a>	Added supply voltage range at RJ-45. Added footnote on maximum recommended operating supply voltage.

New document: "Cinterion® Java Terminal Hardware Interface Description" Version 01

Chapter	What is new
--	Initial document setup.



# 1 Introduction

This document<sup>1</sup> describes the hardware of the Cinterion® Java Terminal:

- **PLS62T-W USB** implements a USB 2.0 interface with a USB-B connector
- **PLS62T-W LAN** implements an Ethernet interface with a RJ-45 8-pin connector

All LTE Terminals contains a Cinterion® PLS62-W module and implements a V.24 / V.28 RS-232 interface with a D-sub 9-pole female socket as well as a 6-pole Western jack as plug-in power supply connector.

Wherever necessary and appropriate this document distinguishes between these two variants.

[Table 1](#) gives a short overview of the available interfaces of the Java Terminal.

**Table 1:** Cinterion® Java Terminal overview

Module/Interface	PLS62T-W USB	PLS62T-W LAN
Cinterion® module	PLS62-W	PLS62-W
RS-232 (Sub-D)	✓	✓
USB (USB-B)	✓	-
GPIO interface GPIOs, Power, ASC1, SPI, I <sup>2</sup> C	✓	✓
Ethernet (RJ-45)	-	✓
Power supply (RJ-11)	✓	✓
2 RF antenna	✓	✓

The scope of this document includes interface specifications, electrical as well as mechanical characteristics of the Java Terminal. It specifies standards pertaining to wireless applications and outlines requirements that must be adhered to for successful product design. The Java Terminal is a compact GSM/UMTS/LTE modem for the transfer of data in GSM/UMTS/LTE networks. Industrial standard interfaces and an integrated SIM card reader allow using the Java Terminal easily as GSM/GPRS/UMTS terminal.

1. The document is effective only if listed in the appropriate Release Notes as part of the technical documentation delivered with your Thales product.

## 1.1 Related Documents

- [1] AT Command Set for your Java Terminal product
- [2] Release Notes for your Java Terminal product

## 1.2 Terms and Abbreviations

**Table 2:** Terms and abbreviations

Abbreviation	Description
ARP	Antenna Reference Point
ATC	AT Command
BTS	Base Transceiver Station
CB	Cell Broadcast
CODEC	Coder-Decoder
DAI	Digital Audio Interface
DCE	Data Circuit terminating Equipment
DSR	Data Set Ready
DTR	Data Terminal Ready
EFR	Enhanced Full Rate
EGSM	Enhanced GSM
EMC	Electromagnetic Compatibility
ESD	Electrostatic Discharge
ETS	European Telecommunication Standard
FDMA	Frequency Division Multiple Access
G.C.F.	GSM Conformity Forum
GSM	Global Standard for Mobile Communication
HW	Hardware
I <sup>2</sup> C	Inter Integrated Circuit
IC	Integrated Circuit
IF	Intermediate Frequency
IMEI	International Mobile Equipment Identifier
I/O	Input/ Output
IGT	Ignition
ISO	International Standards Organization
ITU	International Telecommunications Union
kbps	kbits per second
LVD	Low voltage Directive
Mbps	Mbits per second

## 1.2 Terms and Abbreviations

**Table 2:** Terms and abbreviations

<b>Abbreviation</b>	<b>Description</b>
MCU	Microprocessor Control Unit
MMI	Machine Machine Interface
MO	Mobile Originated
MS	Mobile Station
MT	Mobile Terminated
NC	Not Connected
NTC	Negative Temperature Coefficient
PA	Power Amplifier
PCB	Printed Circuit Board
PCM	Pulse Code Modulation
PCS	Personal Communication System
PD	Power Down
PDU	Protocol Data Unit
PoE	Power over Ethernet
RED	Radio Equipment Directive
RF	Radio frequency
RI	Ring Indication
RX	Receive direction
SIM	Subscriber Identification Module
SMS	Short Message Service
SW	Software
TDD	Time Division Duplex
TDMA	Time Division Multiple Access
TX	Transmit direction
UART	Universal Asynchronous Receiver and Transmitter

## 1.3 Regulatory and Type Approval Information

## 1.3 Regulatory and Type Approval Information

## 1.3.1 Directives and Standards

Java Terminal have been designed to comply with the directives and standards listed below.

Table 3: Directives

2014/53/EU	Directive of the European Parliament and of the council of 16 April 2014 on the harmonization of the laws of the Member States relating to the making available on the market of radio equipment and repealing Directive 1999/ 05/EC. The product is labeled with the CE conformity mark - see <a href="#">Section 6.3</a> .
2002/95/EC (RoHS 1) 2011/65/EU (RoHS 2) 2015/863/EU (RoHS 3)	Directive of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment (RoHS). Revised on 8 June 2011. Further revision on 31 March 2015 - amending Annex II to Directive 2011/ 65/EU of the European Parliament and of the Council as regards the list of restricted substances. 
2002/96/EC	Directive of the European Parliament and of the Council on waste electrical and electronic equipment (WEEE)
2003/108/EC	Directive of the European Parliament and of the Council of 8 December 2003 amending directive 2002/96/ec on waste electrical and electronic equipment (WEEE)

Table 4: Standards of North American type approval

CFR Title 47	"Code of Federal Regulations, Part 15 B, Part 22 and Part 24 (Telecommunications, PCS)"; US Equipment Authorization FCC
OET Bulletin 65 (Edition 97-01)	Evaluating Compliance with FCC Guidelines for Human Exposure to Radiofrequency Electromagnetic Fields
UL 60 950-1	Product Safety Certification (Safety requirements)
NAPRD.03 V5.32	"Overview of PCS Type certification review board Mobile Equipment Type Certification and IMEI control" PCS Type Certification Review board (PTCRB)
RSS102 RSS132 RSS133	Canadian Standard
IEEE Std. C95.1-1999	IEEE Standard for Safety Levels with Respect to Human Exposure to Radio Frequency Electromagnetic Fields, 3 kHz to 300 GHz

Table 5: Standards of European type approval

3GPP TS 51.010-1	"Digital cellular telecommunications system (Phase 2); Mobile Station (MS) conformance specification"
ETSI EN 301 511 V12.5.1	Global System for Mobile communications (GSM); Mobile Stations (MS) equipment; Harmonized Standard covering the essential requirements of article 3.2 of Directive 2014/53/EU
GCF-CC V3.71	Global Certification Forum - Certification Criteria

## 1.3 Regulatory and Type Approval Information

**Table 5:** Standards of European type approval


ETSI EN 301 489-1 V2.2.0	ElectroMagnetic Compatibility (EMC) standard for radio equipment and services; Part 1: Common technical requirements; Harmonized Standard covering the essential requirements of article 3.1(b) of Directive 2014/53/EU and the essential requirements of article 6 of Directive 2014/30/EU
ETSI EN 301 489-52 V1.1.0	Electromagnetic Compatibility (EMC) standard for radio equipment and services; Part 52: Specific conditions for Cellular Communication Mobile and portable (UE) radio and ancillary equipment; Harmonized Standard covering the essential requirements of article 3.1(b) of Directive 2014/53/EU
EN 55032:2015	Electromagnetic compatibility of multimedia equipment - Emission Requirements (CISPR 32:2015)
ETSI EN 301 908-1 V11.1.1	IMT cellular networks; Harmonized Standard covering the essential requirements of article 3.2 of the Directive 2014/53/EU; Part 1: Introduction and common requirements
ETSI EN 301 908-2 V11.1.2	IMT cellular networks; Harmonized Standard covering the essential requirements of article 3.2 of the Directive 2014/53/EU; Part 2: CDMA Direct Spread (UTRA FDD) User Equipment (UE)
ETSI EN 301 908-13 V11.1.2	IMT cellular networks; Harmonised Standard covering the essential requirements of article 3.2 of Directive 2014/53/EU; Part 13: Evolved Universal Terrestrial Radio Access (E-UTRA) User Equipment (UE)
EN 62311-2008	Assessment of electronic and electrical equipment related to human exposure restrictions for electromagnetic fields (0 Hz - 300 GHz)
EN 62368-1:2014 + AC:2015	Safety of information technology equipment

**Table 6:** Requirements of quality

IEC 60068	Environmental testing
DIN EN 60529	IP codes. The Java Terminal comply with IP40.

1.3 Regulatory and Type Approval Information

**Table 7:** Standards of the Ministry of Information Industry of the People’s Republic of China

SJ/T 11363-2006	“Requirements for Concentration Limits for Certain Hazardous Substances in Electronic Information Products” (2006-06).
SJ/T 11364-2006	<p>“Marking for Control of Pollution Caused by Electronic Information Products” (2006-06).</p> <p>According to the “Chinese Administration on the Control of Pollution caused by Electronic Information Products” (ACPEIP) the EPUP, i.e., Environmental Protection Use Period, of this product is 20 years as per the symbol shown here, unless otherwise marked. The EPUP is valid only as long as the product is operated within the operating limits described in the Hardware Interface Description.</p> <p>Please see <a href="#">Table 1.3.2</a> for an overview of toxic or hazardous substances or elements that might be contained in product parts in concentrations above the limits defined by SJ/T 11363-2006.</p> 

**Table 8:** Toxic or hazardous substances or elements with defined concentration limits

部件名称 Name of the part	有毒有害物质或元素 Hazardous substances					
	铅 (Pb)	汞 (Hg)	镉 (Cd)	六价铬 (Cr(VI))	多溴联苯 (PBB)	多溴二苯醚 (PBDE)
金属部件 (Metal Parts)	○	○	○	○	○	○
电路模块 (Circuit Modules)	X	○	○	○	○	○
电缆及电缆组件 (Cables and Cable Assemblies)	○	○	○	○	○	○
塑料和聚合物部件 (Plastic and Polymeric parts)	○	○	○	○	○	○






O:  
表示该有毒有害物质在该部件所有均质材料中的含量均在SJ/T11363-2006 标准规定的限量要求以下。  
Indicates that this toxic or hazardous substance contained in all of the homogeneous materials for this part is below the limit requirement in SJ/T11363-2006.

X:  
表示该有毒有害物质至少在该部件的某一均质材料中的含量超出SJ/T11363-2006标准规定的限量要求。  
Indicates that this toxic or hazardous substance contained in at least one of the homogeneous materials used for this part *might exceed* the limit requirement in SJ/T11363-2006.




1.3 Regulatory and Type Approval Information

**1.3.2 Safety Precautions**

The following safety precautions must be observed during all phases of the operation, usage, service or repair of any cellular terminal or mobile incorporating Java Terminal. Manufacturers of the cellular terminal are advised to convey the following safety information to users and operating personnel and incorporate these guidelines into all manuals supplied with the product. Failure to comply with these precautions violates safety standards of design, manufacture and intended use of the product. THALES DIS AIS Deutschland GmbH assumes no liability for customer's failure to comply with these precautions.

	<p>When in hospitals or other health care facilities, observe the restrictions on the use of mobiles. Switch off the cellular terminal or mobile if to be instructed to do so by the guidelines posted in sensitive areas. Medical equipment may be sensitive to RF energy.</p> <p>The operation of cardiac pacemakers, other implanted medical equipment and hearing aids can be affected by interference from cellular terminals or mobiles placed close to the device. If in doubt about potential danger, contact the physician or the manufacturer of the device to verify that the equipment is properly shielded. Pacemaker patients are advised to keep their hand-held mobile away from the pacemaker, while it is on. This personal subgroup always should check the distance to the mobile.</p>
	<p>Switch off the cellular terminal or mobile before boarding an aircraft. Make sure it cannot be switched on inadvertently. The operation of wireless appliances in an aircraft is forbidden to prevent interference with communications systems. Failure to observe these instructions may lead to the suspension or denial of cellular services to the offender, legal action, or both.</p> <p>Check the local and actual laws about these themes.</p>
	<p>Do not operate the cellular terminal or mobile in the presence of flammable gases or fumes. Switch off the cellular terminal when you are near petrol stations, fuel depots, chemical plants or where blasting operations are in progress. Operation of any electrical equipment in potentially explosive atmospheres can constitute a safety hazard.</p>
	<p>Your cellular terminal or mobile receives and transmits radio frequency energy while switched on. Remember that interference can occur if it is used close to TV sets, radios, computers or inadequately shielded equipment. Follow any special regulations and always switch off the cellular terminal or mobile wherever forbidden, or when you suspect that it may cause interference or danger.</p>
	<p>Road safety comes first! Do not use a hand-held cellular terminal or mobile while driving a vehicle unless it is securely mounted in a holder for speakerphone operation. Before making a call with a hand-held terminal or mobile park the vehicle. Speakerphones must be installed by qualified personnel. Faulty installation or operation can constitute a safety hazard.</p> <p>Check the actual and local laws about these themes.</p>

1.3 Regulatory and Type Approval Information

	<p><b>IMPORTANT!</b> Cellular terminals or mobiles operate using radio signals and cellular networks. In that case connections cannot be guaranteed at all times under all conditions. Therefore, you should never rely solely upon any wireless device for essential communications, for example emergency calls.</p> <p>Remember, in order to make calls or receive calls the cellular terminal or mobile must be switched on in a service area with adequate cellular signal strength.</p> <p>Some networks do not allow for emergency calls if certain network services or phone features are in use (e.g. lock functions, fixed dialing etc.). You may need to deactivate those features before you can make an emergency call. Some networks require a valid SIM card to be properly inserted in the cellular terminal or mobile.</p>
	<p>If a power supply unit is used to supply the device it must meet the demands placed on SELV circuits in accordance with EN60950. The maximum permissible connection length between the device and the supply source should not exceed 3m.</p>
	<p>According to the guidelines for human exposure to radio frequency energy, an antenna connected to the FME jack of the device should be placed at least 20cm away from human bodies.</p>



### 1.4 Product Label

The label fixed to the bottom of a Java Terminal comprises the following information.



Figure 1: Sample Java Terminal label PLS62T-W USB

Table 9: Java Terminal label information

No.	Information
1	Cinterion logo
2	Product name
3	Product variant
4	Marking "Made in Germany"
5	Product ordering number
6	Bar code (Code128)
7	Product IMEI
8	Date code
9	WEEE symbol (see Table 3)
10	Chinese RoHS symbol (see Table 7)
11	CE logo. May be replaced for samples with "Not for sale" (see also Section 6.3)
12	FCC ID for Java module contained in Java Terminal
13	IC ID for Java module contained in Java Terminal
14	Manufacturer code
15	Power supply unit ratings
16	Manufacturer code (2D)

## 2 Product Concept

### 2.1 Key Features at a Glance

Feature	Implementation
General	
Incorporates Cinterion® Java module	The Java module handles all signal and data processing within the Java Terminal. Internal software runs the application interface and the complete GSM/UMTS/LTE protocol stack.
Frequency bands	GSM/GPRS/EDGE: Quad band GSM: 850/900/1800/1900MHz  UMTS/HSPA+ (I, II, IV, V, VIII, IX, XIX): seven band: UMTS 800/850/900/(1700/2100)/1800/1900/2100MHz  LTE: (1, 2, 3, 4, 5, 7, 8, 12, 18, 19, 20, 28): Twelve band LTE: 700/800/850/900/1700/2100/1800/1900/2100/2600MHz
GSM class	Small MS
Output power (according to release 99)	Class 4 (+33dBm ±2dB) for EGSM850 Class 4 (+33dBm ±2dB) for EGSM900 Class 1 (+30dBm ±2dB) for GSM1800 Class 1 (+30dBm ±2dB) for GSM1900 Class E2 (+27dBm ± 3dB) for GSM 850 8-PSK Class E2 (+26dBm ± 3dB) for GSM 900 8-PSK Class E2 (+26dBm +3 /-4dB) for GSM 1800 8-PSK Class E2 (+26dBm +3 /-4dB) for GSM 1900 8-PSK
Output power (according to Release 8)	Class 3 (+24dBm +1/-3dB) for UMTS 800, WCDMA FDD BdXIX Class 3 (+24dBm +1/-3dB) for UMTS 850, WCDMA FDD BdV Class 3 (+24dBm +1/-3dB) for UMTS 900, WCDMA FDD BdVIII Class 3 (+24dBm +1/-3dB) for UMTS AWS, WCDMA FDD BdIV Class 3 (+24dBm +1/-3dB) for UMTS 1800, WCDMA FDD BdIX Class 3 (+24dBm +1/-3dB) for UMTS 1900, WCDMA FDD BdII Class 3 (+24dBm +1/-3dB) for UMTS 2100, WCDMA FDD BdI
Output power (according to Release 9)	Class 3 (+23dBm ±2dB) for LTE 700, LTE FDD Bd12 <MFBI Bd17> Class 3 (+23dBm ±2dB) for LTE 700, LTE FDD Bd28 Class 3 (+23dBm ±2dB) for LTE 800, LTE FDD Bd18 Class 3 (+23dBm ±2dB) for LTE 800, LTE FDD Bd19 Class 3 (+23dBm ±2dB) for LTE 800, LTE FDD Bd20 Class 3 (+23dBm ±2dB) for LTE 850, LTE FDD Bd5 Class 3 (+23dBm ±2dB) for LTE 900, LTE FDD Bd8 Class 3 (+23dBm ±2dB) for LTE AWS, LTE FDD Bd4 Class 3 (+23dBm ±2dB) for LTE 1800, LTE FDD Bd3 Class 3 (+23dBm ±2dB) for LTE 1900, LTE FDD Bd2 Class 3 (+23dBm ±2dB) for LTE 2100, LTE FDD Bd1 Class 3 (+23dBm ±2dB) for LTE 2600, LTE FDD Bd7
Power supply	Single supply voltage 8V to 57V (Power-over-Ethernet support)
Operating temperature (Java module board temperature)	Normal operation: -30°C to +85°C Extended operation: -40°C to -30°C and +85°C to +90°C

## 2.1 Key Features at a Glance

Feature	Implementation
Physical	Dimensions: 113.5mm x 75mm x 25.5mm (excluding antenna and serial interface connectors) Weight: 120g (approx.)
RoHS, WEEE	All hardware components are fully compliant with the EU RoHS and WEEE Directives
LTE features	
3GPP Release 9	UE CAT 1 supported DL 10.2Mbps, UL 5.2Mbps
HSPA features	
3GPP Release 6,7	DL 7.2Mbps, UL 5.7Mbps HSDPA Cat.8 / HSUPA Cat.6 data rates Compressed mode (CM) supported according to 3GPP TS25.212
UMTS features	
3GPP Release 4	PS data rate – 384 kbps DL / 384 kbps UL CS data rate – 64 kbps DL / 64 kbps UL
GSM / GPRS / EDGE features	
Data transfer	GPRS: <ul style="list-style-type: none"> <li>• Multislot Class 12</li> <li>• Full PBCCH support</li> <li>• Mobile Station Class B</li> <li>• Coding Scheme 1 – 4</li> </ul> EGPRS: <ul style="list-style-type: none"> <li>• Multislot Class 12</li> <li>• EDGE E2 power class for 8 PSK</li> <li>• Downlink coding schemes – CS 1-4, MCS 1-9</li> <li>• Uplink coding schemes – CS 1-4, MCS 1-9</li> <li>• SRB loopback and test mode B</li> <li>• 8-bit, 11-bit RACH</li> <li>• PBCCH support</li> <li>• 1 phase/2 phase access procedures</li> <li>• Link adaptation and IR</li> <li>• NACC, extended UL TBF</li> <li>• Mobile Station Class B</li> </ul> CSD: <ul style="list-style-type: none"> <li>• V.110, RLP, non-transparent</li> <li>• 2.4, 4.8, 9.6, 14.4kbps</li> <li>• USSD</li> </ul>
SMS	<ul style="list-style-type: none"> <li>• Point-to-point MT and MO</li> <li>• Cell broadcast</li> <li>• Text and PDU mode</li> </ul>
Software	
AT commands	Hayes 3GPP TS 27.007, TS 27.005, Thales M2M

## 2.1 Key Features at a Glance

Feature	Implementation
Java™ Open Platform	<p>Java™ Open Platform with</p> <ul style="list-style-type: none"> <li>• Java™ profile IMP-NG &amp; CLDC 1.1 HI</li> <li>• Secure data transmission via HTTPS/SSL</li> <li>• Multi-threading programming and multi-application execution</li> </ul> <p>Major benefits: seamless integration into Java applications, ease of programming, no need for application micro-controller, extremely cost-efficient hardware and software design – ideal platform for industrial GSM applications.</p> <p>The memory space available for Java programs is around 31MB in the flash file system and around 18MB RAM. Application code and data share the space in the flash file system and in RAM.</p>
SIM Application Toolkit	SAT Release 99
TCP/IP stack	Protocols: TCP server/client, UDP, DNS, Ping, HTTP, FTP, SMTP Access by AT commands
Firmware update	Upgradeable via serial ASC0 (RS-232) or USB interface
<b>Interfaces</b>	
USB interfaces	PLS62T-W USB: USB 2.0 Slave interface
Ethernet	PLS62T-W LAN: 8-pole RJ-45 Ethernet connector Support for Power over Ethernet (PoE)
RS232	<p>RS-232 interface for AT commands and data:</p> <ul style="list-style-type: none"> <li>• Supports RTS/CTS hardware handshake</li> <li>• Supports software XON/XOFF flow control</li> <li>• Multiplex ability according to GSM 07.10 Multiplexer protocol</li> <li>• Baud rates from 1200bps to 230400bps</li> <li>• Autobauding supported</li> </ul>
GPIO connector	20-pin (8-pin and 12-pin) header with GPIO interface, external power supply, serial interface ASC1, ADC, SPI and I <sup>2</sup> C
Power connector	6-pole Western connector (female) for power supply, ignition, power down signal
SIM card reader	Supported SIM cards: 3V, 1.8V
Antenna	2 Antenna connected via female SMA connector
<b>Power on/off, Reset</b>	
Power on	DTR line at RS-232 interface, IGT_IN line at power connector or watchdog
Power off	Normal switch-off by AT^SMSO Automatic switch-off in case of critical temperature conditions
Reset	Orderly shutdown and reset by AT command Emergency restart via RST_IN line at power connector or via watchdog
<b>Special features</b>	
Real time clock	Timer functions via AT commands
Phonebook	SIM card and terminal
(Hardware) Watchdog	Configurable watchdog to control module

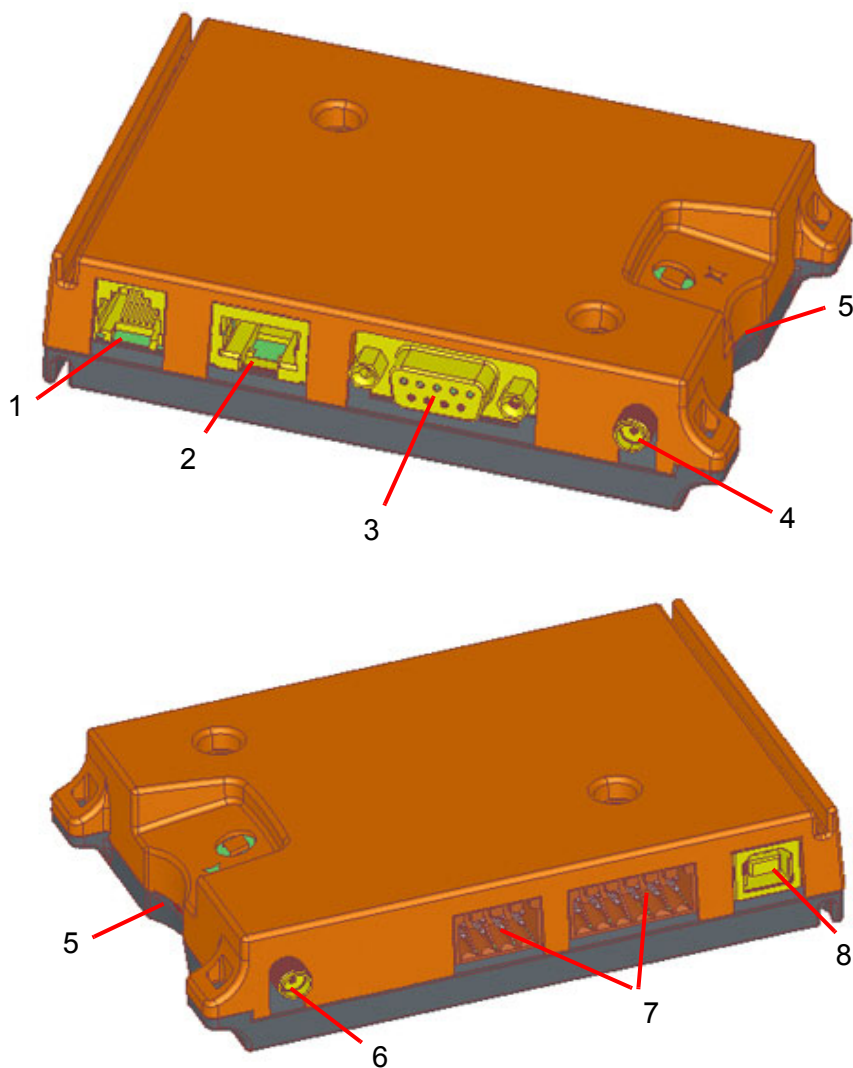
## 3 Interface Description

### 3.1 Overview

Java Terminal provide the following interfaces for power supply, antenna, SIM card and data transfer:

**Table 10:** Java Terminal' interfaces

No.	Description
1	6-pin Western connector (female) for power supply, ignition, power down signal
2	8-pin (female) RJ-45 Ethernet connector (PLS62T-W LAN only)
3	9-pin (female) D-sub connector (RS-232 interface)
4	SMA connector (female) for Rx diversity antenna
5	SIM card reader
6	SMA connector (female) for RF main antenna
7	12-pin and 8-pin GPIO connectors (male)
8	4-pin (female) USB-B connector (PLS62T-W USB only)



**Figure 2:** Java Terminal 3D view

3.2 Block Diagram

### 3.2 Block Diagram

Figure 3 shows a block diagram of a sample configuration that incorporates a Java Terminal and typical accessories.

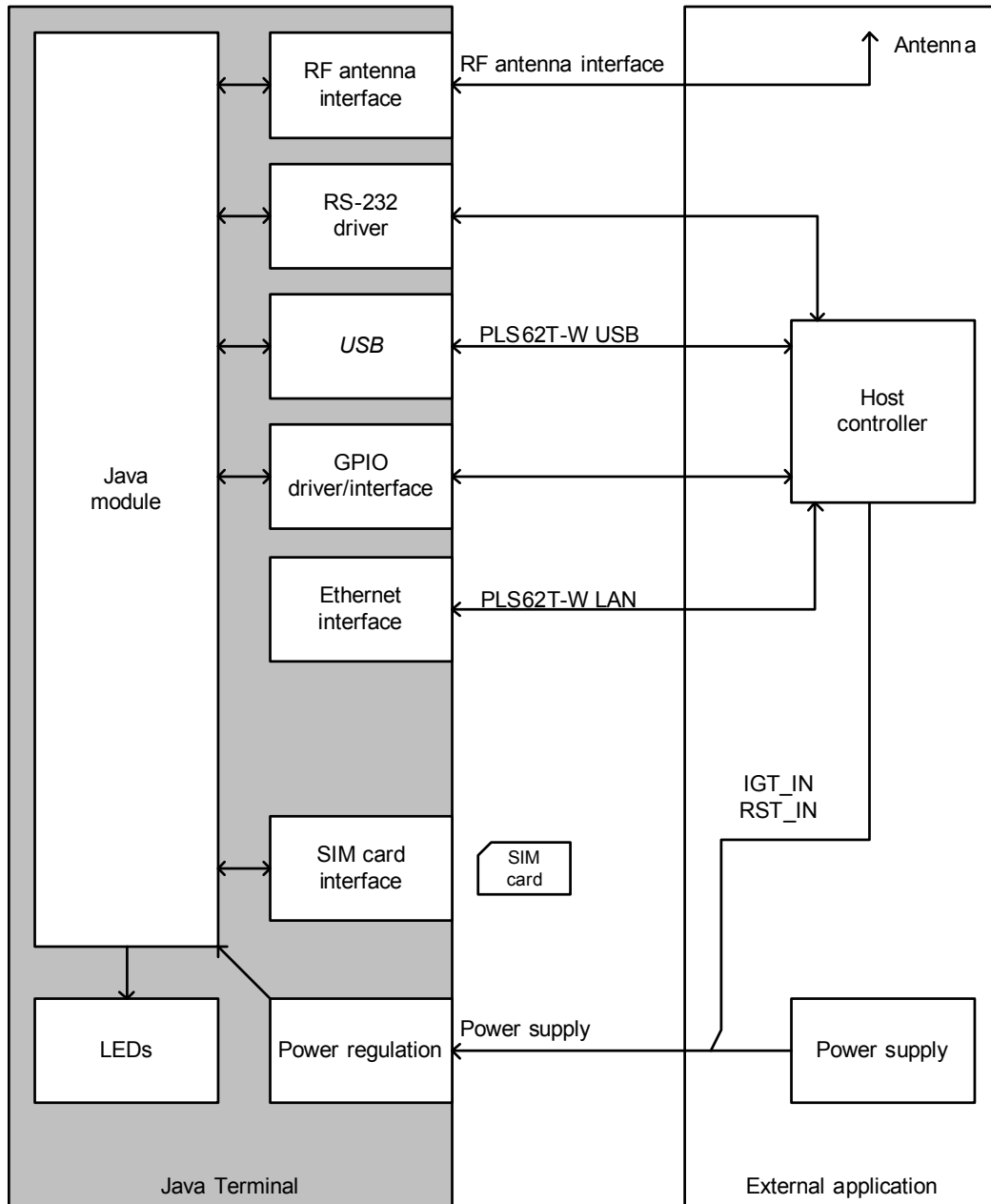


Figure 3: Block diagram

3.3 Terminal Circuit

### 3.3 Terminal Circuit

Figure 4 shows a general Java Terminal block diagram that includes all variants. Not every interface is available for all Terminal products.

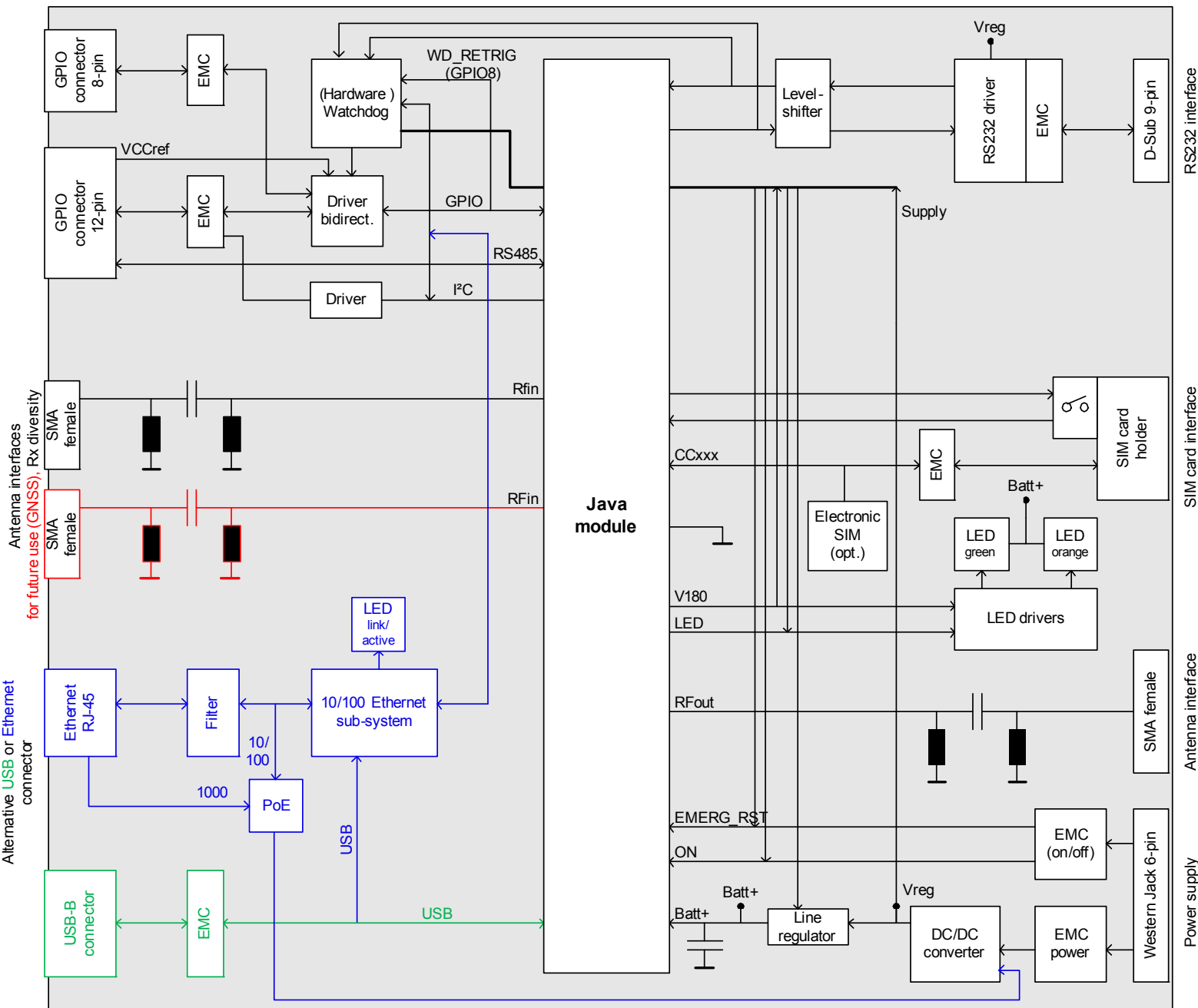


Figure 4: Java Terminal circuit block diagram

### 3.4 Operating Modes

The table below briefly summarizes the various operating modes referred to in the following chapters.

**Table 11:** Overview of operating modes

Normal operation	GSM IDLE	Software is active. Once registered to the GSM network paging with BTS is carried out. The Terminal is ready to send and receive. Watchdog active.
	GSM TALK GSM DATA	Connection between two subscribers is in progress. Power consumption depends on network coverage individual settings, such as DTX off/on, FR/EFR/HR, hopping sequences, antenna. Watchdog active.
	GPRS/UMTS/HSPA IDLE	Terminal is ready for GPRS data transfer, but no data is currently sent or received. Power consumption depends on network settings and GPRS configuration (e.g. multislot settings). Watchdog active.
	GPRS DATA	GPRS data transfer in progress. Power consumption depends on network settings (e.g. power control level), uplink / downlink data rates, GPRS configuration (e.g. used multislot settings) and reduction of maximum output power. Watchdog active.
	EGPRS DATA	EGPRS data transfer in progress. Power consumption depends on network settings (e.g. power control level), uplink / downlink data rates, EGPRS configuration (e.g. used multislot settings) and reduction of maximum output power. Watchdog active.
	UMTS TALK UMTS DATA	UMTS data transfer in progress. Power consumption depends on network settings (e.g. TPC Pattern) and data transfer rate. Watchdog active.
	HSPA DATA	HSPA data transfer in progress. Power consumption depends on network settings (e.g. TPC Pattern) and data transfer rate. Watchdog active.
Power Down	Normal shutdown after sending the AT^SMSO command. The RTC works continuously, but the software is not active. Interfaces are not accessible. Watchdog continues to operate, depending on its configuration.	



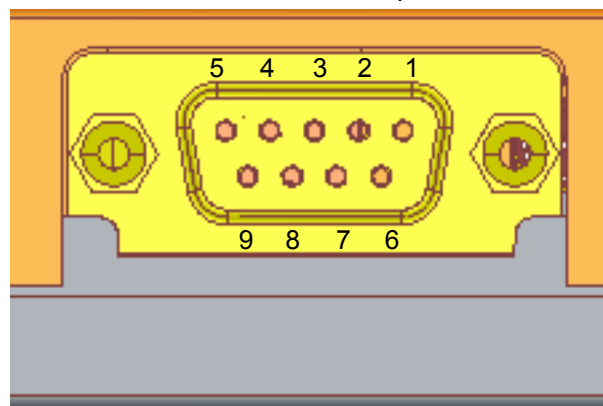
## 3.5 RS-232 Interface

The interface is implemented as a serial asynchronous transmitter and receiver conforming to ITU-T V.24 Interchange Circuits DCE. It is configured for 8 data bits, no parity and 1 stop bit, and can be operated at bit rates from 1200bps to 921kbps. Autobauding supports bit rates from 1.2kbps to 230kbps.

For more information see also [Section 3.5.1](#).

### 3.5.1 9-Pole D-sub Connector

Via RS-232 interface, the host controller controls the Java Terminal and transports data.



**Figure 5:** Pin assignment RS-232 (D-sub 9-pole female)

**Table 12:** 9-pole D-sub (female) RS-232

Pin no.	Signal name	I/O	Function
1	DCD0	O	Data Carrier Detected
2	RXD0	O	Receive Data
3	TXD0	I	Transmit Data
4	DTR0	I	Data Terminal Ready Attention: The ignition of Java Terminal is activated via a rising edge of high potential (+3 ... +15 V)
5	GND	-	Ground
6	DSR0 <sup>1</sup>	O	Data Set Ready
7	RTS0	I	Request To Send
8	CTS0	O	Clear To Send
9	RING0	O	Ring Indication

1. Note that DSR0 may also be available via GPIO connector pin - see [Section 3.8](#).

Java Terminal are designed for use as a DCE. Based on the conventions for DCE-DTE connections it communicates with the customer application (DTE) using the following signals:

- Port TxD @ application sends data to TXD0 of the Java Terminal
- Port RxD @ application receives data from RXD0 of the Java Terminal

Hardware handshake using the RTS0 and CTS0 signals and XON/XOFF software flow control are supported.

## 3.6 USB Interface (PLS62T-W USB)

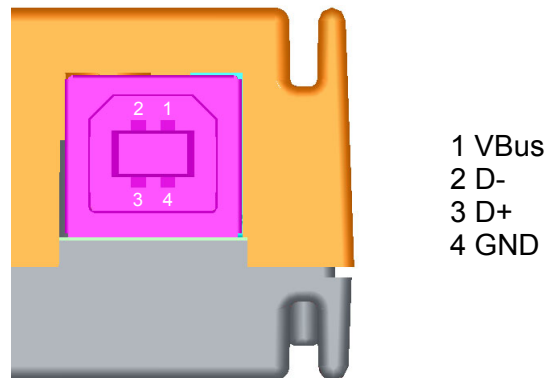
In addition, the modem control signals DTR0, DSR0, DCD0 and RING0 are available. The modem control signal RING0 (Ring Indication) can be used to indicate, to the cellular device application, that a call or Unsolicited Result Code (URC) is received. There are different modes of operation, which can be set with AT commands.

**Note:** The DTR0 signal will only be polled once per second from the internal firmware of Java Terminal.

### 3.6 USB Interface (PLS62T-W USB)

The Java Terminal PLS62T-W USB support a USB 2.0 High Speed (480Mbit/s) device interface that is Full Speed (12Mbit/s) compliant.

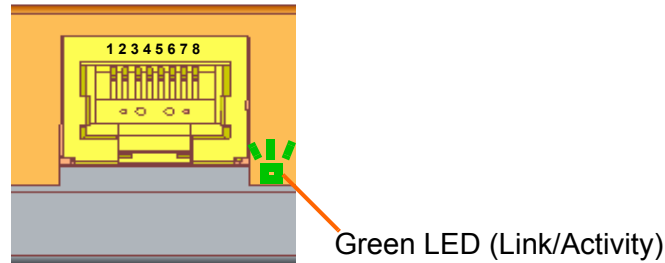
The USB interface can be used as command and data interface and for downloading Java module firmware. It is only available as a slave device and not able to act as a USB host.



**Figure 6:** USB Type B receptacle interface

### 3.7 Ethernet Interface (PLS62T-W LAN)

The Ethernet interface is available only on PLS62T-W LAN.



**Figure 7:** 8-pole RJ-45 Ethernet connector (female)

**Table 13:** Female 8-pole RJ-45 Ethernet connector

Pin	Signal name	Use	PoE Mode-A	PoE Mode-B
1	TX+	Transmit +	DC-	
2	TX-	Transmit -	DC-	
3	RX+	Receive +	DC+	
4	n/c	Reserved		DC+
5	n/c	Reserved		DC+
6	RX-	Receive +	DC+	
7	n/c	Reserved		DC-
8	n/c	Reserved		DC-

The Ethernet interface can be used as transparent data interface, for downloading new LTE Terminal firmware releases - module firmware as well as Ethernet sub-system software, and also to access the implemented LTE module via AT command interface.

The Ethernet interface supports Power over Ethernet (i.e., PoE+ (Class 0) according to IEEE 802.3at, and backward compatible to IEEE 802.3af.). This means that if connected to a PoE enabled router or PoE injector, the LTE Terminal does no longer need a separate power supply unit such as the mains adapter mentioned in [Section 3.9](#). The mains adapter and PoE may be connected in parallel, but one of the supplies suffices to operate the LTE Terminal (the other may be used a power supply backup). Power supplies shall be of type PS1, according to IEC 62368-1.

PLS62T-W LAN supports Power over Ethernet in Mode-A (“Phantom Power”) on the data lines RX/TX and Mode-B, where the power is supplied via separate Pins 4, 5, 7 and 8. The polarity of the PoE shown in [Table 13](#) is the default configuration, but PLS62T-W LAN don’t care about the polarity of the PoE.

**Note:** The maximum cable length between LTE Terminal and the nearest network socket should be 10m, and it is recommended that the cable should be shielded.

Using the Ethernet interface it is also possible to maintain a database containing possible APNs

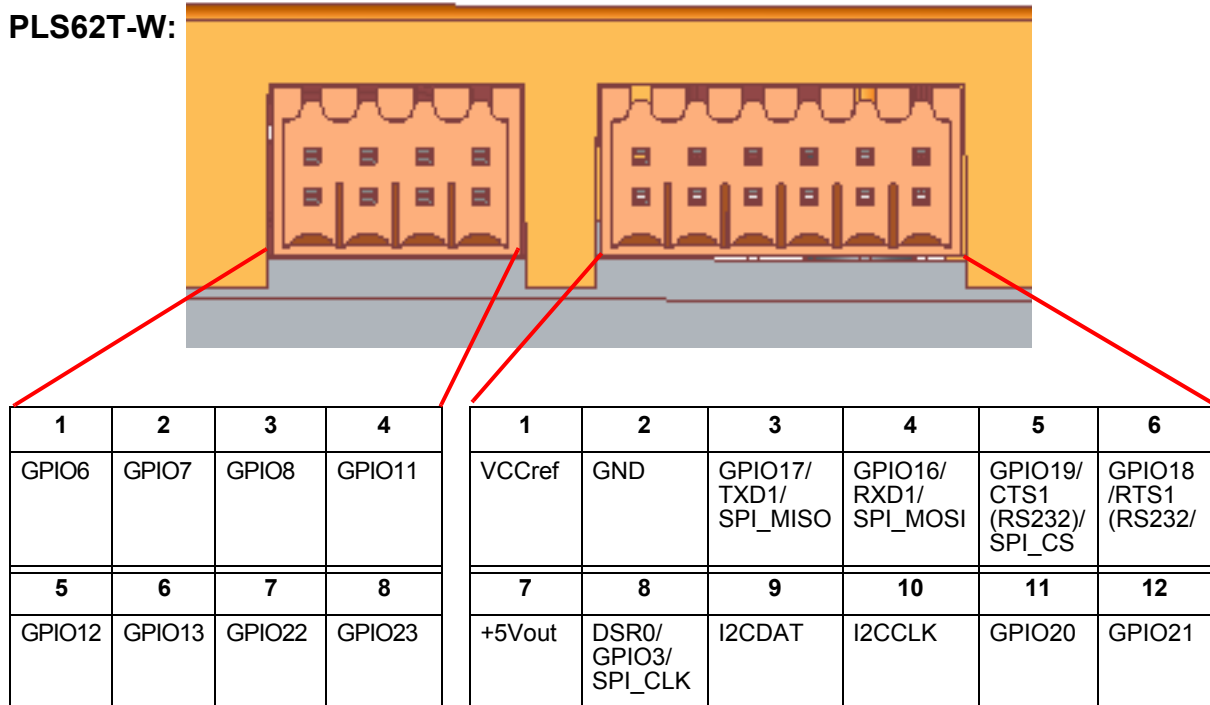
3.8 GPIO Interface

for the LTE Terminal. Please refer to [Appendix B: Ethernet Setup and Usage](#) for details on how to setup and use Java Terminal with Ethernet interface. For further characteristics of Ethernet interface refer to [Section 4.2.4](#).

A green LED near to the RJ-45 Ethernet connector shows the Link/Activity status of the Ethernet interface (refer to [Figure 7](#)).

### 3.8 GPIO Interface

The GPIO connectors (8-pin and 12-pin) provide access to various module signals including a number of configurable GPIOs. Note that not all of the pins are available for every Java Terminal variant. The following figures show the available pins for the Java Terminal variants and the below [Table 14](#) lists the overall availability of the pins.



n/a: not applicable

**Figure 8:** PLS62T-W USB: GPIO connectors (8-pin and 12-pin)

## 3.8 GPIO Interface

The following [Table 14](#) shows the availability of the GPIO connector pins for the Java Terminal.

**Table 14:** GPIO connector pin availability and alternate pin functionalities

PIN	Signal	Comment	PLS62T-W
8-pin connector			
1	GPIO6	Configurable via AT command, also as PWM2 signal	✓
2	GPIO7	Configurable via AT command, also as PWM1 signal	✓
3	GPIO8	Configurable via AT command, also as COUNTER signal	✓
4	GPIO11	Configurable via AT command	✓
5	GPIO12	Configurable via AT command	✓
6	GPIO13	Configurable via AT command	✓
7	GPIO22	Configurable via AT command	✓
8	GPIO23	Configurable via AT command	✓
12-pin connector			
1	VCCref	Input supply for level adjustment. E.g., connect +5Vout for 5V IO operation	✓
2	GND		✓
3	GPIO17, TXD1 or SPI_MISO	Configurable via AT command, GPIO or TXD1 (RS-232) or SPI_MISO signal	GPIO17 /TXD1 /SPI_MISO
4	GPIO16, RXD1 or SPI_MOSI	Configurable via AT command, GPIO, RXD1 (RS-232) or SPI_MOSI signal	GPIO16 /RXD1 /SPI_MOSI
5	GPIO19, CTS1 or SPI_CS	Configurable via AT command, GPIO, CTS1 (RS-232) or SPI_CS signal	GPIO19 /CTS1 /SPI_CS
6	GPIO18 or RTS1	Configurable via AT command, GPIO or RTS1 (RS-232) signal	GPIO18 /RTS1
7	+5Vout	External power supply up to 100mA, usable as VCCref input	✓
8	DSR0, GPIO3 or SPI_CLK	Configurable via AT command	DSR0 /GPIO3 /RTS1
9	I2CDAT	I <sup>2</sup> C interface	✓
10	I2CCLK	I <sup>2</sup> C interface	✓
11	GPIO20	Configurable via AT command	✓
12	GPIO21	Configurable via AT command	✓

Please refer to the “AT Command Set” for details on how to configure the GPIO pins using AT commands.

### 3.8.1 Serial Interface ASC1

The Java Terminal has four pins at the GPIO connector which can be configured as ASC1 interface signals to provide a 2- or 4-wire serial modem interface ASC1. For electrical characteristics please refer to [Table 21](#).

The following pins at the GPIO connector can be configured as ASC1 interface signals: RXD1, TXD1, RTS1 and CTS1 (see also [Table 14](#)). The configuration is done by AT command AT^SCFG - see [\[1\]](#). It is non-volatile and becomes active after a Java Terminal restart.

Java Terminal are designed for use as a DCE. Based on the conventions for DCE-DTE connections they communicate with the customer application (DTE) using the following signals:

- Port TXD @ application sends data to Java Terminal's TXD1 signal line
- Port RXD @ application receives data from the Java Terminal's RXD1 signal line

### 3.8.2 I<sup>2</sup>C Interface

Two lines of the Java Terminal' GPIO connector are I<sup>2</sup>C lines. I<sup>2</sup>C is a serial, 8-bit oriented data transfer bus for bit rates up to 400kbps in Fast mode. It consists of two lines, the serial data line I2CDAT and the serial clock line I2CCLK. The Java Terminal' internal Java module act as a single master device, e.g. the clock I2CCLK is driven by the Java module. I2CDAT is a bi-directional line. Each device connected to the bus is software addressable by a unique 7-bit address, and simple master/slave relationships exist at all times. The Java module operates as master-transmitter or as master-receiver. An external application transmits or receives data only on request of the module.

To configure and activate the I2C bus use the AT^SSPI command. Detailed information on the AT^SSPI command as well explanations on the protocol and syntax required for data transmission can be found in [\[1\]](#).

With the external application, I2CDAT and I2CCLK lines need to be connected to a positive supply voltage via a pull-up resistor. For electrical characteristics please refer to [Table 21](#).

Note: Good care should be taken when connecting the I2C lines to the external application: The wires of I2CCLK and I2CDAT should be equal in length and as short as possible.

### 3.8.3 SPI Interface

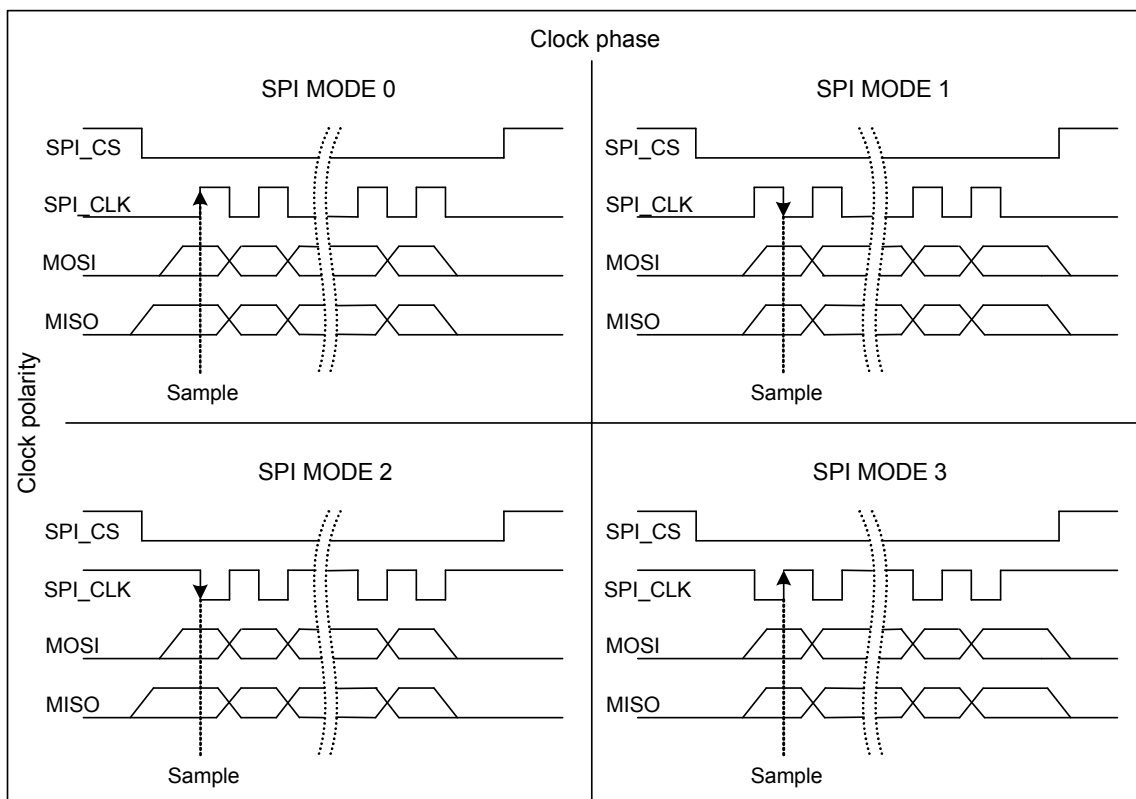
For the Java Terminal four GPIO interface pins can be configured as Serial Peripheral Interface (SPI). The SPI is a synchronous serial interface for control and data transfer between Java Terminal and the external application. Only one application can be connected to the SPI and the interface supports only master mode. The transmission rates are up to 6.5Mbit/s. The SPI interface comprises the two data lines MOSI and MISO, the clock line SPI\_CLK a well as the chip select line SPI\_CS.

The following pins at the GPIO connector can be configured as SPI interface signals: SPI\_CLK, SPI\_MOSI, SPI\_MISO and SPI\_CS (see also [Table 14](#)). The configuration is done by AT command (see [\[1\]](#)). It is non-volatile and becomes active after a module restart. For electrical characteristics please refer to [Table 21](#).

To configure and activate the SPI interface use the AT<sup>SSPI</sup> command. Detailed information on the AT<sup>SSPI</sup> command as well explanations on the SPI modes required for data transmission can be found in [\[1\]](#). SPI, GPIO and DSR0 functionalities are mutually exclusive.

In general, SPI supports four operation modes. The modes are different in clock phase and clock polarity. The module's SPI mode can be configured by using the AT command AT<sup>SSPI</sup>. Make sure the module and the connected slave device works with the same SPI mode.

[Figure 9](#) shows the characteristics of the four SPI modes. The SPI modes 0 and 3 are the most common used modes.



**Figure 9:** Characteristics of SPI modes

### 3.8.4 PWM Interface

The GPIO6 and GPIO7 pins at the GPIO connector can be configured as Pulse Width Modulation signals PWM1 and PWM2 by AT^SCFG command - see [1]. The PWM interface lines are output lines and can be used, for example, to connect buzzers. The PWM1 line is shared with GPIO7 and the PWM2 line is shared with GPIO6 (see also Table 14). GPIO and PWM functionalities are mutually exclusive<sup>1</sup>.

### 3.8.5 Pulse Counter

The GPIO8 pin at the GPIO connector can be configured as pulse counter line COUNTER by AT^SCFG command - see [1]. The pulse counter interface is an input line and can be used, for example, as a clock (see also Table 14). GPIO and COUNTER functionalities are mutually exclusive<sup>1</sup>.

### 3.8.6 DSR0 signal

The DSR0 line available at the RS-232 interface is also routed to the GPIO connector and available as a separate pin. For electrical characteristics see Section 4.2.4. DSR0 can be configured by AT^SCFG command - see [1]. DSR0, GPIO and SPI functionalities are mutually exclusive.

---

1. **Note:** For the PWM interface and pulse counter to operate, the directions of the Java Terminal' GPIO level shifters must be set according to the defined directions of the PWM and COUNTER signals. See Section 8.3.1.11 and Section 8.5 for more information on how to change level shifter directions.



## 3.9 Power Supply

### 3.9 Power Supply

The Java Terminal power supply via Western jack has to be a single voltage source of  $V_{PLUS}=8V\dots 57V$  capable of providing a peak current (pulsed  $2\times 577ms$  at  $T=4.615ms$ ) of about  $1.2A$  at  $8V$  during an active transmission<sup>1</sup>. The uplink burst causes strong ripple (drop) on the power lines. The drop voltage should not exceed  $1V$ . The absolute minimum voltage during drops is given in [Section 4.1](#).

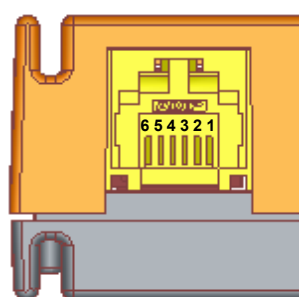
The Java Terminal are protected from supply voltage reversal. An external fast acting fuse  $\geq 0.4A$  with melting integral  $I^2t$  ( $0.15 \dots 0.25$ ) $A^2s$  is necessary to use the Java Terminal at a  $12V$  or  $24V$  unlimited power supply system.

The power supply must be compliant with the EN60950 guidelines. A switching regulator regulates the input voltage for the internal supply.

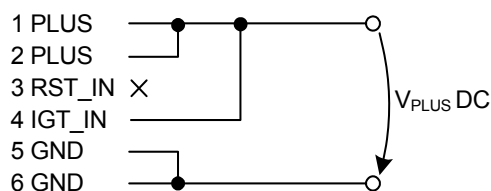
When power fails for  $>1ms$ , Java Terminal reset or switch off. The watchdog can be configured to restart the Java Terminal. When power fails for  $>15s$  the RTC will be reset.

**Table 15:** Female 6-pole Western plug for power supply, ignition, power down

Pin	Signal name	Use	Parameters
1	PLUS	Power supply	$8V - 57V$ DC
2	PLUS	Power supply	$8V - 57V$ DC
3	RST_IN	Signal for module reset	$U_{IH} \geq 8V$ for $t > 10ms$ resets the terminal. $U_{IL} < 2V$ and low level for normal operation.
4	IGT_IN	Ignition	$U_{IH} \geq 8V$ Ignition $\geq 8V$ for more than $200ms$ switches the Java Terminal on. Ignition is activated only by a rising edge. The rise time is $< 20ms$
5	GND	Ground	$0V$
6	GND	Ground	$0V$



Pin assignment and typical connection:



**Figure 10:** 6-pole Western jack for power supply, ignition, reset, typical connection

Mains adapter: If it fits into the design of your application we recommend the plug-in supply unit used with the type approved Thales reference setup. Ordering information can be found in

1. As an alternative to the power supply via Western jack, Power over Ethernet (PoE) can be employed. The PoE feature complies with IEEE 802.3 standards, and can be made available in Ethernet networks via PoE enabled devices such as PoE routers or injectors.

[Chapter 7](#). This 12V mains adapter comes with a 6-pole Western plug and provides an internal connection between IGT\_IN pin and PLUS pin. If there is such an “auto ignition” connection between PLUS and IGT\_IN, the module will restart automatically after a normal shutdown.

## 3.10 Power Up/Power Down Scenarios

In general, be sure not to turn on the Java Terminal while it is beyond the safety limits of voltage and temperature stated in [Section 4.1](#). Java Terminal immediately switch off after having started and detected these inappropriate conditions. In extreme cases this can cause permanent damage to the Java Terminal.

### 3.10.1 Turn Java Terminal on

There are various possibilities to turn on Java Terminal and start into normal mode, depending on connecting and/or operating states.

If powered off (i.e., if Java Terminal' power supply is disconnected):

- Java Terminal can simply be started up by plugging an appropriate power supply unit between PLUS and GND of the 6-pole Western jack.

If switched off (i.e., while in Power Down mode):

- Java Terminal can be started up by activating the IGT\_IN signal (pin 4).
- Java Terminal can be started up by activating the RS-232 DTR line (as long as RST\_IN signal (pin 3) is not active (voltage <2V)
- Java Terminal can be started up by configuring the watchdog appropriately. For details see [Chapter 8](#).

Note: With an “auto-ignition” connection between IGT\_IN and PLUS, or if employing PoE (see [Section 3.9](#)), Java Terminal will automatically restart into normal mode after switch off (see [Section 3.10.3](#)). The start up variant from Power Down mode is therefore only applicable if there is no such “auto-ignition” connection at the power supply unit.

### 3.10.2 Reset/Restart Java Terminal

There are various possibilities to reset/restart Java Terminal:

- Java Terminal can be reset/restarted by entering the command AT+CFUN=x,1. For details on AT+CFUN please see [\[1\]](#).
- Java Terminal can be reset/restarted by configuring the watchdog appropriately. For details see [Chapter 8](#).
- Java Terminal can be reset/restarted by configuring a wake-up alarm (see [Section 3.13](#)) before using AT^SMSO to turn them off and send them into Power Down mode.
- Java Terminal can be reset/restarted by simply shutting them down as described in [Section 3.10.3](#) and then restarting them as described in [Section 3.10.1](#). Note: With an “auto ignition” connection between IGT\_IN and PLUS, it is ensured that the module will automatically restart after a normal shutdown using AT^SMSO.

Emergency restart:

- Finally, Java Terminal can be reset/restarted in the event of software hang-ups etc. by

applying a voltage >8V to the RST\_IN pin (pin 3) for more than 10ms. The RST\_IN signal then restarts the Java Terminal.

Caution: Use the RST\_IN pin only when, due to serious problems, the software is not responding for more than 5 seconds. Pulling the RST\_IN pin causes the loss of all information stored in the volatile memory since power is cut off immediately. Therefore, this procedure is intended only for use in case of emergency, e.g. if Java Terminal fails to shut down properly.

### 3.10.3 Turn Java Terminal off

Normal shutdown:

- To turn off the Java Terminal use the AT^SMSO command, rather than disconnecting the mains adapter.

This switch off procedure lets the Java Terminal log off from the network and allows the software to enter a secure state and save data before disconnecting the power supply. After AT^SMSO has been entered the Java Terminal returns the following result codes:

```
^SMSO: MS OFF
OK
^SHUTDOWN
```

The "^SHUTDOWN" result code indicates that the Java Terminal turns off in less than 1 second. After the shutdown procedure is complete the Java Terminal enters the Power Down mode. The yellow LED stops flashing (see [Section 3.15](#) for a detailed LED description). The RTC is still fed from the voltage regulator in the power supply ASIC. Please note that if there is an "auto ignition" connection between PLUS and IGT\_IN, the module will restart automatically after a normal shutdown.

### 3.10.4 Disconnecting power supply

Before disconnecting the power supply from the PLUS pin, make sure that the Java Terminal are in a safe condition. The best way is to wait 1s after the "^SHUTDOWN" result code has been indicated.

### 3.11 Automatic thermal shutdown

An on-board NTC measures the temperature of the built-in Java module. If over- or undertemperature is detected on the module the Java Terminal automatically shut down to avoid thermal damage to the system. [Table 23](#) specifies the ambient temperature threshold for the Java Terminal.

The automatic shutdown procedure is equivalent to the power-down initiated with the AT^SM-SO command, i.e. Java Terminal log off from the network and the software enters a secure state avoiding loss of data.

Alert messages transmitted before the Java Terminal switch off are implemented as Unsolicited Result codes (URCs). For details see the description of AT^SCTM command provided in [\[1\]](#).

The watchdog can be configured to restart the Java Terminal after a defined period of time.

### 3.12 Hardware Watchdog

The Java Terminal feature a programmable hardware watchdog that permanently monitors the terminals hardware and can be configured to react to various hardware states. The watchdog may for example be configured to periodically restart the terminal, independent of its current operating state. [Figure 4](#) shows how the watchdog is integrated into the Java Terminal.

Please refer to [Chapter 8](#) for details on how to control and configure the hardware watchdog.

### 3.13 RTC

The internal Real Time Clock (RTC) of the Java Terminal retains the time and date and handles the alarm function. The AT+CCLK command serves to set the time and date, and AT+CALA specifies a reminder message or sets an alarm for the Java Terminal to wake up. See [\[1\]](#) for details.

A dedicated voltage regulator backs up the RTC even in Power Down mode and enables Java Terminal to keep track of time and date.

However, please note that the Alarm function described in [\[1\]](#), Section AT+CALA, will only work if there is no “auto ignition” connection active between IGT\_IN pin and PLUS pin at the power supply unit that will automatically trigger a restart (see [Section 3.9](#)). Otherwise, the AT+CALA command can only be used to set a reminder message, but not to configure the Java Terminal to wake up from Power Down mode.

### 3.14 SIM Interface

The SIM interface is intended for 1.8V and 3V SIM cards in accordance with GSM 11.12 Phase 2. The card holder is a five wire interface according to GSM 11.11. A sixth pin has been added to detect whether or not a SIM card is inserted.

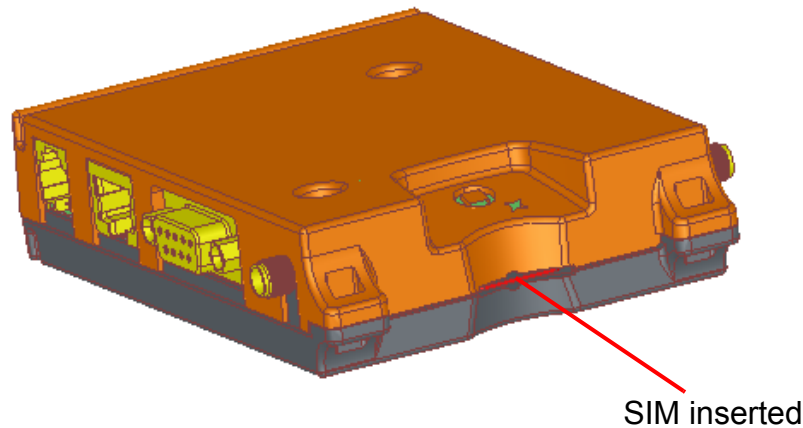


Figure 11: SIM interface

The SIM - with the circuit side facing upwards - is inserted by gently pushing it into the SIM card holder until it snaps hold. It is now protected from accidental removal. The SIM can be removed from the card holder by using a flat object such as a screwdriver to carefully press the inserted SIM until it snaps out again.

All signals of the SIM interface are protected from electrostatic discharge.

Removing and inserting the SIM card during operation requires the software to be reinitialized. Therefore, after reinserting the SIM card it is necessary to restart Java Terminal.

**Note:** No guarantee can be given, nor any liability accepted, if loss of data is encountered after removing the SIM card during operation. Also, no guarantee can be given for properly initializing any SIM card that the user inserts after having removed a SIM card during operation. In this case, the application must restart the Java Terminal.

### 3.15 Status LEDs

Java Terminal have two LEDs indicating its operating states through the semitransparent casing:

- A green LED indicates whether the Java Terminal are ready to operate and reports certain watchdog operations.
- A orange LED indicates the network registration state of the Java Terminal.

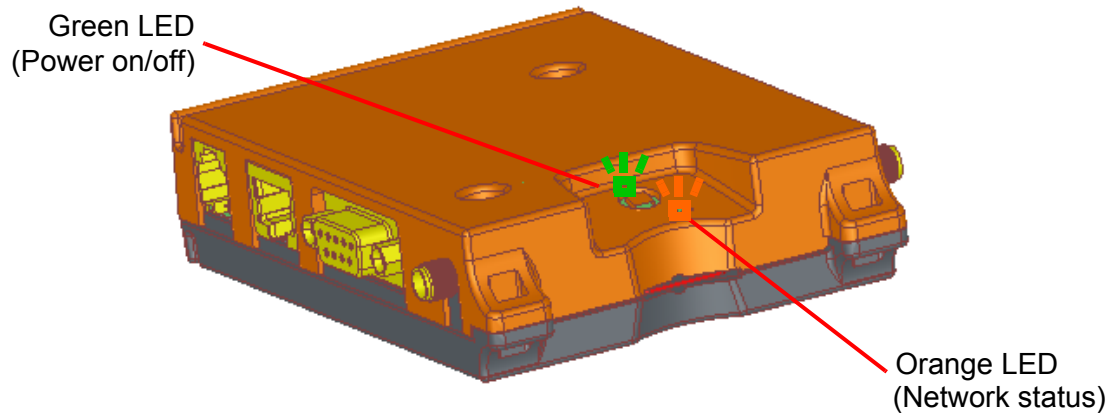


Figure 12: Status LED

The orange LED can be configured by using the AT^SLED command to either light permanently or to flash. For details on the AT command please refer to [\[1\]](#).

### 3.16 RF Antenna Interface

The external RF antennas are connected via the Java Terminal's female SMA connectors that is also the antenna reference point (ARP).

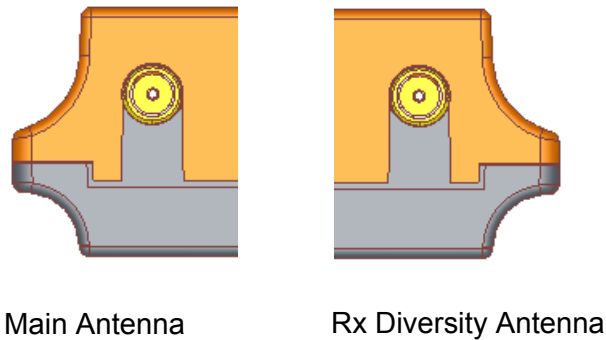


Figure 13: Antenna connectors

The system impedance is 50Ω. In any case, for good RF performance, the return loss of the customer application's antenna should be better than 10dB (VSWR < 2). Java Terminal withstand a total mismatch at this connector when transmitting with maximum RF power.

Additional ESD protection to the antenna connector is provided. No DC voltage must be applied to the antenna circuit to protect it from damage.

Please note that the terminal should be installed and operated with a minimum distance of 20cm between the antenna connected to the terminal and any human bodies. Also, the transmitter must not be co-located or operating in conjunction with any other antenna or transmitter. The allowed maximum antenna gain (including cable loss) for stand-alone situation is given below in Table 16.

Table 16: Allowed maximum antenna gain (including cable loss)

Region	700 MHz	800 MHz	850 MHz	900 MHz	1700/2100 MHz	1800 MHz	1900 MHz	2100 MHz	2600 MHz
EN 62311: 2008	8.68dBi	4.46dBi		4.46dBi		10.35dBi		11.38dBi	12.01dBi
FCC	2.00dbi		5.15dBi		2.15dBi		2.15dBi		4.20dBi

## 4 Electrical and Environmental Characteristics

### 4.1 Absolute Maximum Ratings

Table 17: Absolute maximum ratings

Parameter	Port / Description	Min.	Max.	Unit
Supply voltage	PLUS	-40	57 <sup>1</sup>	V
Overvoltage	PLUS / for 1min		33	V
Input voltage for on/off control lines	IGT_IN, RST_IN	-5	30	V
RS-232 input voltage	TXD0, DTR0, RTS0	-25	+25	V
GPIO connector pins input voltage (incl. VCCref)	8-pin and 12-pin connectors (if pins specified/configured as input pins)	-0.3	6	V
GPIO connector pins output current	8-pin and 12-pin connectors (if pins specified/configured as output pins)	0	50mA drawn @each pin <sup>2</sup>	--
USB interface	All electrical characteristics according to USB Implementers' Forum, USB 2.0 Specification.	--	--	--
Immunity against discharge of static electricity	All interfaces (lines) Contact discharge Air discharge	-4 -8	+4 +8	kV kV

1. Maximum 50V recommended.

2. Please note that if the VCCref pin is connected to the +5Vout pin, no more than 100mA should be drawn by **all** pins. In this case it is no longer allowed to draw a maximum of 50mA for each pin.

Table 18: Operating supply voltage for Java Terminal

Parameter	Min	Typ	Max	Unit
Supply voltage PLUS measured at (6-pole) western jack plug (1 to 6)	8	12	57 <sup>1</sup>	V
Minimum supply voltage PLUS measured at (6-pole) western jack plug (1 to 6) @any time, incl. all ripple, drops and temperature fluctuations	7.6			V
Supply voltage at RJ-45 for PoE according to IEEE 802.3at via PoE enabled router or injector	44	48	57 <sup>1</sup>	V

1. Maximum 50V recommended.



## 4.2 Electrical Specifications of the Application Interface

### 4.2.1 On/Off Control

**Table 19:** On/Off control line specifications

Parameter	Description	Conditions	Min.	Typ	Max.	Unit
$V_{high}$	Input voltage IGT_IN, RST_IN	active high	8		30	V
$V_{low}$			0		2	V
$R_{IN}$	Input resistance of IGT_IN, RST_IN		1			M $\Omega$
$V_{high}$	Input voltage DTR	active high	2.4		+25	V
$V_{low}$			-25		0.8	V
$R_{IN}$	Input resistance of DTR		3	5	7	k $\Omega$

### 4.2.2 RS-232 Interface

**Table 20:** RS-232 interface specifications

Parameter	Description	Conditions	Min.	Typ	Max.	Unit
$V_{OUT}$	Transmitter output voltage swing for RXD0, CTS0, DSR0, DCD0, RING0	@ 3k $\Omega$ load	$\pm 5$	$\pm 5.4$		V
$R_{OUT}$	Transmitter output resis- tance RXD0, CTS0, DSR0, DCD0, RING0		300			$\Omega$
$V_{in}$	Receiver input voltage range TXD0, RTS0, DTR0		-25		+25	V
$R_{IN}$	Receiver input resistance TXD0, RTS0, DTR0		3	5	7	k $\Omega$
$V_{low}$	Input threshold low		0.8	1.5		V
$V_{high}$	Input threshold high			1.8	2.4	V
Baudrate		Autobauding	1.2		960	kbps
		Fixed range	1.2		960	kbps
$LE_{Cable}$	Length of RS-232 cable			1.8	2	m

### 4.2.3 USB Interface (PLS62T-W USB)

All electrical characteristics according to USB Implementers' Forum, USB 2.0 Specification.

## 4.2 Electrical Specifications of the Application Interface

## 4.2.4 GPIO Interface

Table 21: GPIO interface specifications (requirements)

Function	Signal name	IO	Signal form and level	Comment
8-pin, 12-pin connectors for: GPIO, Power, I <sup>2</sup> C and ASC1, SPI	GPIO 6-8 GPIO 11-13 GPIO 20-21 GPIO22-23	IO	$V_{OLmax} = 0.1V$ at $I = 100\mu A$ $V_{OLmax} = 0.55V$ at $I = 32mA$ $V_{OHmin} = VCCref - 0.1V$ at $I = 100\mu A$ $V_{OHmin} = VCCref - 0.4V$ at $I < 12mA$ $V_{OHmin} = VCCref - 0.7V$ at $I < 32mA$ $V_{ILmax} = 0.3 * VCCref$ $V_{IHmin} = 0.7 * VCCref$	<p>If unused keep lines open.</p> <p>Please note that some GPIO lines can be configured for functions other than GPIO:  GPIO6/GPIO7: PWM  GPIO8: Pulse Counter/  WD_RETRIG</p>
	VCCref	I	$V_{imax} = 5.5V$ $V_{imin} = 1.8V$  Input current has to at least match the positive output current required by all configured and used GPIO lines.	<p>For level adjustment. E.g., connect 5Vout for 5V IO operation.</p> <p>Must be connected to 1.8...5.5V if employing signals from GPIO connector.</p>
	GND	--	--	
	GPIO17/ TXD1/ SPI_MISO	IO	$V_{OLmax} = 0.1V$ at $I = 100\mu A$ $V_{OLmax} = 0.55V$ at $I = 32mA$ $V_{OHmin} = VCCref - 0.1V$ at $I = 100\mu A$ $V_{OHmin} = VCCref - 0.4V$ at $I < 12mA$ $V_{OHmin} = VCCref - 0.7V$ at $I < 32mA$ $V_{ILmax} = 0.3 * VCCref$ $V_{IHmin} = 0.7 * VCCref$	<p>If unused keep lines open.</p>
	GPIO16/ RXD1/ SPI_- MOSI	IO		
	GPIO19/ CTS1/ SPI_CS	IO		
	GPIO18/ RTS1	IO		
	DSR0/GPIO3/ SPI_CLK	IO		
+5Vout	O	$5V, +0.05V, -0.2V$ $I_{outmax} = 100mA$	<p>Regulated output for external supply. Can be connected to VCCref.</p> <p>If unused, keep open.</p>	

## 4.2 Electrical Specifications of the Application Interface

**Table 21:** GPIO interface specifications (requirements)

Function	Signal name	IO	Signal form and level	Comment
8-pin, 12-pin connectors for: GPIO, Power, I <sup>2</sup> C and ASC1, SPI	I2CDAT	IO	Open drain IO $V_{OLmin} = 0.3V$ at $I = -3mA$ $V_{OHmax} = VCCref$ $R_{pullup} = 2.2k\Omega$  $V_{ILmax} = 0.35V$ $V_{IHmin} = 1.3V$ $V_{IHmax} = VCCref$	According to the I <sup>2</sup> C Bus Specification Version 2.1 for the fast mode a rise time of max. 300ns is permitted. There is also a maximum $V_{OL}=0.4V$ at 3mA specified. The value of the pull-up depends on the capacitive load of the whole system (I <sup>2</sup> C Slave + lines). The maximum sink current of I2CDAT and I2CCLK is 4mA.  If unused keep lines open.
	I2CCLK	IO		

### 4.2.5 Ethernet Interface (PLS62T-W LAN)

The Ethernet interface is implemented as 10/100 Base-T RJ-45 Ethernet according to the IEEE 802.3 Ethernet standards, including PoE+ (IEEE 802.3at, backward compatible to IEEE 802.3af). For further characteristics see also [Section 3.7](#).

Please refer to [Appendix B: Ethernet Setup and Usage](#) for details on how to setup and use the Java Terminal with Ethernet interface.

## 4.3 Power Supply Ratings

## 4.3 Power Supply Ratings

Table 22: Power supply specifications

Parameter	Description	Conditions		PLS62T-W		Unit
$V_{PLUS}$	Allowed voltage ripple (peak-peak), drop during transmit burst peak current	Power control level for $P_{out\ max}^1$		1		V
$I_{PLUS}^2$	Average supply current (average time 3 min.)	Power Down mode	@8V	21	mA	
			@30V	10		
			@57V	---		
	Average GSM supply current (average time 3 min.)	IDLE mode (GSM/GPRS, 850/900MHz, 1800/1900MHz)	@8V	40	mA	
			@30V	16		
			@57V	---		
		GPRS DATA mode(1 Tx, 4 Rx, 850/900MHz)	@8V	175	mA	
			@30V	50		
			@57V	---		
		GPRS DATA mode(1 Tx, 4 Rx, 1800/1900MHz)	@8V	140	mA	
			@30V	38		
			@57V	---		
		GPRS DATA mode(4 Tx, 1 Rx, 850/900MHz)	@8V	245	mA	
			@30V	100		
			@57V	---		
		GPRS DATA mode(4 Tx, 1 Rx, 1800/1900MHz)	@8V	190	mA	
			@30V	56		
			@57V	---		
EDGE DATA mode(1 Tx, 4 Rx, 850/900MHz)	@8V	175	mA			
	@30V	50				
	@57V	---				
EDGE DATA mode(1 Tx, 4 Rx, 1800/1900MHz)	@8V	130	mA			
	@30V	40				
	@57V	---				
EDGE DATA mode(4 Tx, 1 Rx, 850/900MHz)	@8V	240	mA			
	@30V	70				
	@57V	---				
EDGE DATA mode(4 Tx, 1 Rx, 1800/1900MHz)	@8V	190	mA			
	@30V	56				
	@57V	---				

## 4.3 Power Supply Ratings

Table 22: Power supply specifications

Parameter	Description	Conditions			Unit
				PLS62T-W	
I <sub>PLUS</sub>	Peak supply current (during 577µs transmission slot every 4.6ms)	Power control level for Pout max (850/900MHz)	@8V	1130	mA
			@30V	270	
			@57V	---	
		Power control level for Pout max (1800/1900MHz)	@8V	820	mA
			@30V	200	
			@57V	---	
	Average UMTS supply current (average time 3 min.)	IDLE mode	@8V	79	mA
			@30V	12	
			@57V	---	
		UMTS DATA (Band I; 23dBm)	@8V	410	mA
			@30V	115	
			@57V	---	
		UMTS DATA Band II; 23dBm	@8V	450	mA
			@30V	125	
			@57V	---	
		UMTS DATA Band V/VI; 23dBm	@8V	415	mA
			@30V	115	
			@57V	---	
		UMTS DATA Band VIII; 23dBm	@8V	410	mA
			@30V	115	
@57V			---		
HSPA DATA (Band I; 23dBm)		@8V	415	mA	
		@30V	115		
		@57V	---		
HSPA DATA Band II; 23dBm	@8V	450	mA		
	@30V	125			
	@57V	---			
HSPA DATA Band V/VI; 23dBm	@8V	415	mA		
	@30V	115			
	@57V	---			
HSPA DATA Band VIII; 23dBm	@8V	410	mA		
	@30V	115			
	@57V	---			

4.3 Power Supply Ratings

Table 22: Power supply specifications

Parameter	Description	Conditions			Unit
				PLS62T-W	
I <sub>PLUS</sub>	Average LTE supply current (average time 3 min.)  TBD.	IDLE	@8V		mA
			@30V		
			@57V		
		LTE DATA Band 1, 23dBm	@8V		mA
			@30V		
			@57V		
		LTE DATA Band 2, 23dBm	@8V		mA
			@30V		
			@57V		
		LTE DATA Band 3, 23dBm	@8V		mA
			@30V		
			@57V		
		LTE DATA Band 4, 23dBm	@8V		mA
			@30V		
			@57V		
		LTE DATA Band 5, 23dBm	@8V		mA
			@30V		
			@57V		
		LTE DATA Band 8, 23dBm	@8V		mA
			@30V		
			@57V		
		LTE DATA Band 12, 23dBm	@8V		mA
			@30V		
			@57V		
		LTE DATA Band 13, 23dBm	@8V		mA
			@30V		
			@57V		

## 4.3 Power Supply Ratings

**Table 22:** Power supply specifications

Parameter	Description	Conditions			Unit
				PLS62T-W	
I <sub>PLUS</sub>	Average LTE supply current (average time 3 min.)	LTE DATA Band 18, 23dBm	@8V		mA
			@30V		
			@57V		
		LTE DATA Band 19, 23dBm	@8V		mA
			@30V		
			@57V		
		LTE DATA Band 20, 23dBm	@8V		mA
			@30V		
			@57V		
		LTE DATA Band 28, 23dBm	@8V		mA
			@30V		
			@57V		

1. Lowest voltage (minimum peak) incl. all ripple and drops >7.6V including voltage drop, ripple and spikes, measured at western jack (6-pole) pins.
2. Typical values measured with antenna impedance = 50 Ohm (return loss >20dB).

## 4.4 Operating Temperatures

**Table 23:** Temperature characteristics

Parameter	Min	Typical	Max	Unit
Normal operation <sup>1</sup>	-30		+85	°C
Extended operation <sup>1, 2</sup>	-40 to -30		+85 to +90	°C
Automatic thermal shutdown <sup>1, 3</sup>	<-40		>+90	°C
Thermal resistance ( $R_{th}$ ) <sup>4</sup> 2G operation (with $P_{th} = 1.5W$ ) 3G operation (with $P_{th} = 3.5W$ )		10 11.5		K/W

1. Board temperature of Java module.
2. Extended operation allows normal mode speech calls or data transmission for limited time until automatic thermal shutdown takes effect. Within the extended temperature range (outside the normal operating temperature range) the specified electrical characteristics may be in- or decreased.
3. Due to temperature measurement uncertainty, a tolerance of  $\pm 3^{\circ}C$  on these switching thresholds may occur.
4. Thermal resistance ( $R_{th}$ ) of the Java Terminal at the highest possible thermal power ( $P_{th}$ ) dissipation, i.e., at the worst possible network conditions. Measured in still air with an air gap of at least 100mm between the Java Terminal and other objects.

Note: Within the specified operating temperature ranges the board temperature may vary to a great extent depending on operating mode, used frequency band, radio output power and current supply voltage. Note also the differences and dependencies that usually exist between board (PCB) temperature of the Java module and its ambient temperature.



## 4.5 Antenna Interface

Table 24 lists RF antenna interface specifications for the Java Terminal. Please note that the specified conditions may not apply to or be supported by all terminals.

**Table 24:** RF Antenna interface GSM / UMTS / LTE

Parameter	Conditions	Min.	Typical	Max.	Unit
UMTS/HSPA connectivity	Band I, II, V, VI, VIII (not every module variant supports all bands)				
Receiver Input Sensitivity @ ARP	UMTS 800/850 Band VI/V	-104.7/ -106.7	-110		dBm
	UMTS 900 Band VIII	-103.7	-110		dBm
	UMTS 1900 Band II	-104.7	-109		dBm
	UMTS 2100 Band I	-106.7	-110		dBm
RF Power @ ARP with 50Ohm Load Board temperature <85°C	UMTS 800/850 Band VI/V	+21	+24	+25	dBm
	UMTS 900 Band VIII	+21	+24	+25	dBm
	UMTS 1900 Band II	+21	+24	+25	dBm
	UMTS 2100 Band I	+21	+24	+25	dBm
GPRS coding schemes	Class 12, CS1 to CS4				
EGPRS	Class 12, MCS1 to MCS9				
GSM Class	Small MS				
Static Receiver input Sensitivity @ ARP	GSM 850 / E-GSM 900	-102	-109		dBm
	GSM 1800 / GSM 1900	-102	-108		dBm
RF Power @ ARP with 50Ohm Load	GSM		33		dBm
			30		dBm

## 4.5 Antenna Interface

**Table 24:** RF Antenna interface GSM / UMTS / LTE

Parameter	Conditions	Min.	Typical	Max.	Unit	
RF Power @ ARP with 50Ohm Load, (with maximum power reduction)	GPRS, 1 TX	GSM 850 / E-GSM 900		33		dBm
		GSM 1800 / GSM 1900		30		dBm
	EDGE, 1 TX	GSM 850 / E-GSM 900		27		dBm
		GSM 1800 / GSM 1900		26		dBm
	GPRS, 2 TX	GSM 850 / E-GSM 900		30		dBm
		GSM 1800 / GSM 1900		27		dBm
	EDGE, 2 TX	GSM 850 / E-GSM 900		24		dBm
		GSM 1800 / GSM 1900		23		dBm
	GPRS, 3 TX	GSM 850 / E-GSM 900		28.2		dBm
		GSM 1800 / GSM 1900		25.2		dBm
	EDGE, 3 TX	GSM 850 / E-GSM 900		22.2		dBm
		GSM 1800 / GSM 1900		21.2		dBm
	GPRS, 4 TX	GSM 850 / E-GSM 900		27		dBm
		GSM 1800 / GSM 1900		24		dBm
	EDGE, 4 TX	GSM 850 / E-GSM 900		21		dBm
		GSM 1800 / GSM 1900		20		dBm

## 4.6 Storage Conditions

## 4.6 Storage Conditions

Table 25: Storage conditions

Type	Condition	Unit	Reference
Air temperature: Low High	-30 +75	°C	ETS 300 019-2-1: T1.2, IEC 60068-2-1 Ab ETS 300 019-2-1: T1.2, IEC 60068-2-2 Bb
Humidity relative: Low High Condens.	10 90 at 30°C 90-100 at 30°C	%	--- ETS 300 019-2-1: T1.2, IEC 60068-2-56 Cb ETS 300 019-2-1: T1.2, IEC 60068-2-30 Db
Air pressure: Low High	70 106	kPa	IEC TR 60271-3-1: 1K4 IEC TR 60271-3-1: 1K4
Movement of surrounding air	1.0	m/s	IEC TR 60271-3-1: 1K4
Water: rain, dripping, icing and frosting	Not allowed	---	---
Radiation: Solar Heat	1120 600	W/m <sup>2</sup>	ETS 300 019-2-1: T1.2, IEC 60068-2-2 Bb ETS 300 019-2-1: T1.2, IEC 60068-2-2 Bb
Chemically active substances	Not recommended		IEC TR 60271-3-1: 1C1L
Mechanically active substances	Not recommended		IEC TR 60271-3-1: 1S1
Vibration sinusoidal: Displacement Acceleration Frequency range	1.5 5 2-9 9-200	mm m/s <sup>2</sup> Hz	IEC TR 60271-3-1: 1M2
Shocks: Shock spectrum Duration Acceleration	semi-sinusoidal 1 50	ms m/s <sup>2</sup>	IEC 60068-2-27 Ea

The conditions stated above are only valid for devices in their original packed state in weather protected, non-temperature-controlled storage locations. Normal storage time under these conditions is 12 months maximum.

## 5 Mechanics, Mounting and Packaging

### 5.1 Mechanical Dimensions

Figure 14 shows a 3D view of the Java Terminal and provides an overview of the mechanical dimensions of the board. For further details see Figure 15 and Figure 5.2. To allow for an easier mechanical implementation into an external application 3D STP data for the Java Terminals is attached to this PDF. Please open the Attachments navigation panel to view and save this information.

Length: 113.5mm (including fixtures for cable straps)  
Width: 75mm (excluding antenna and serial interface connectors)  
Height: 25.5mm  
  
Weight: 120g

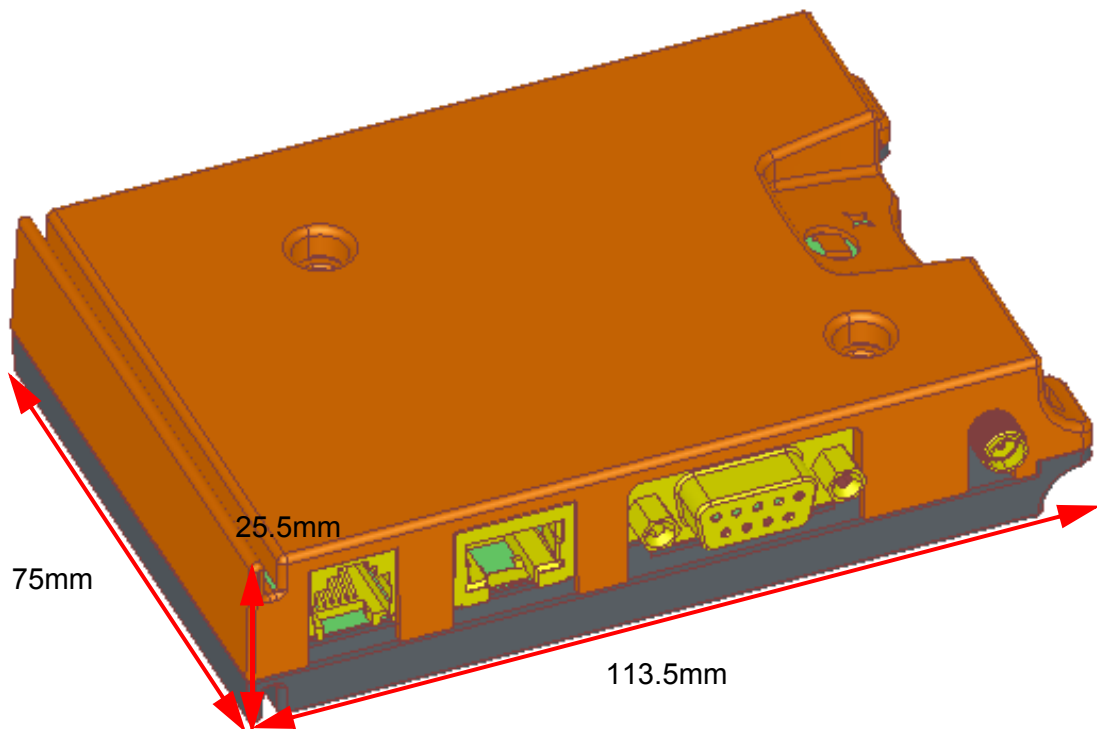


Figure 14: Java Terminal 3D overview

5.1 Mechanical Dimensions

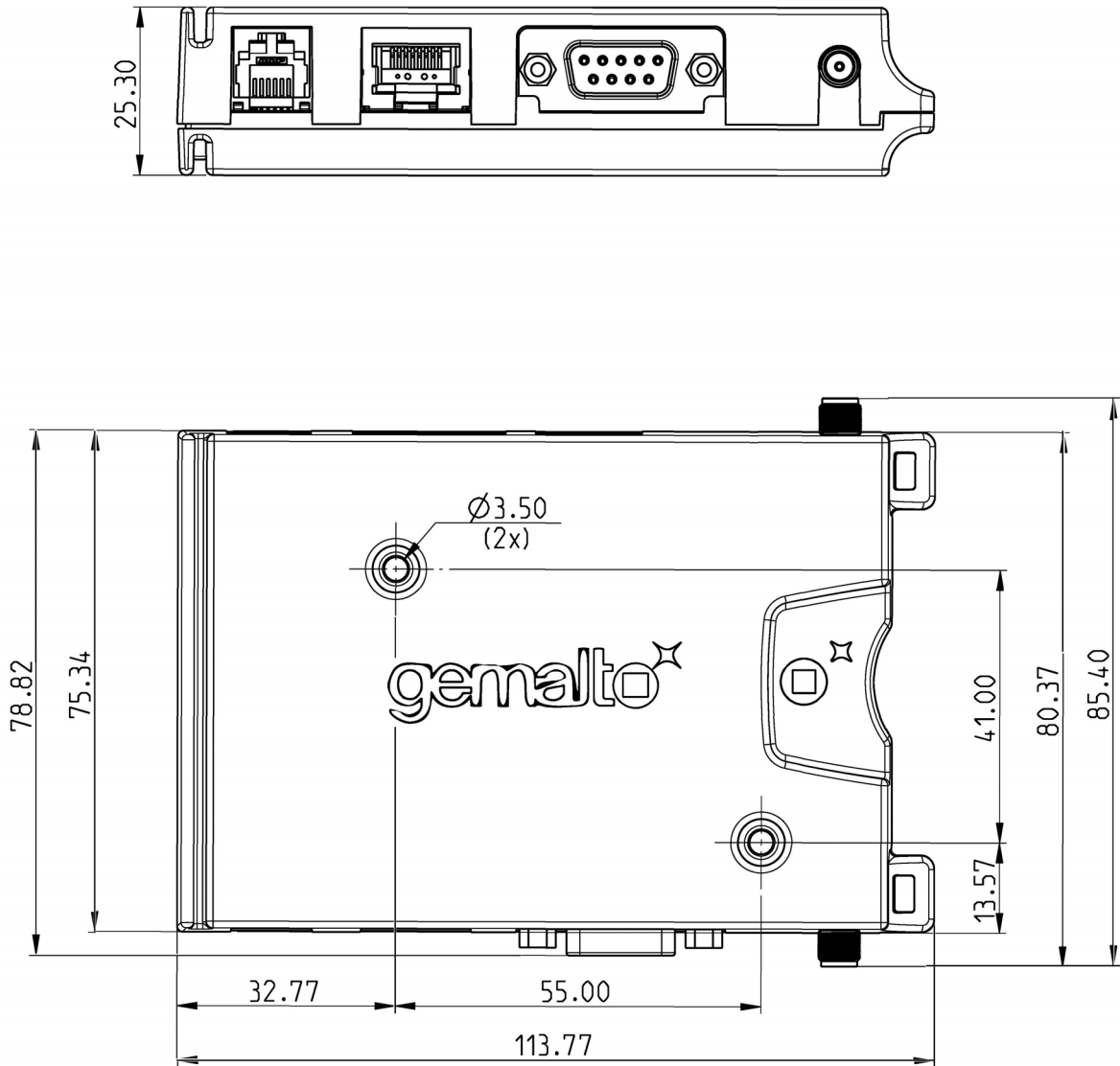


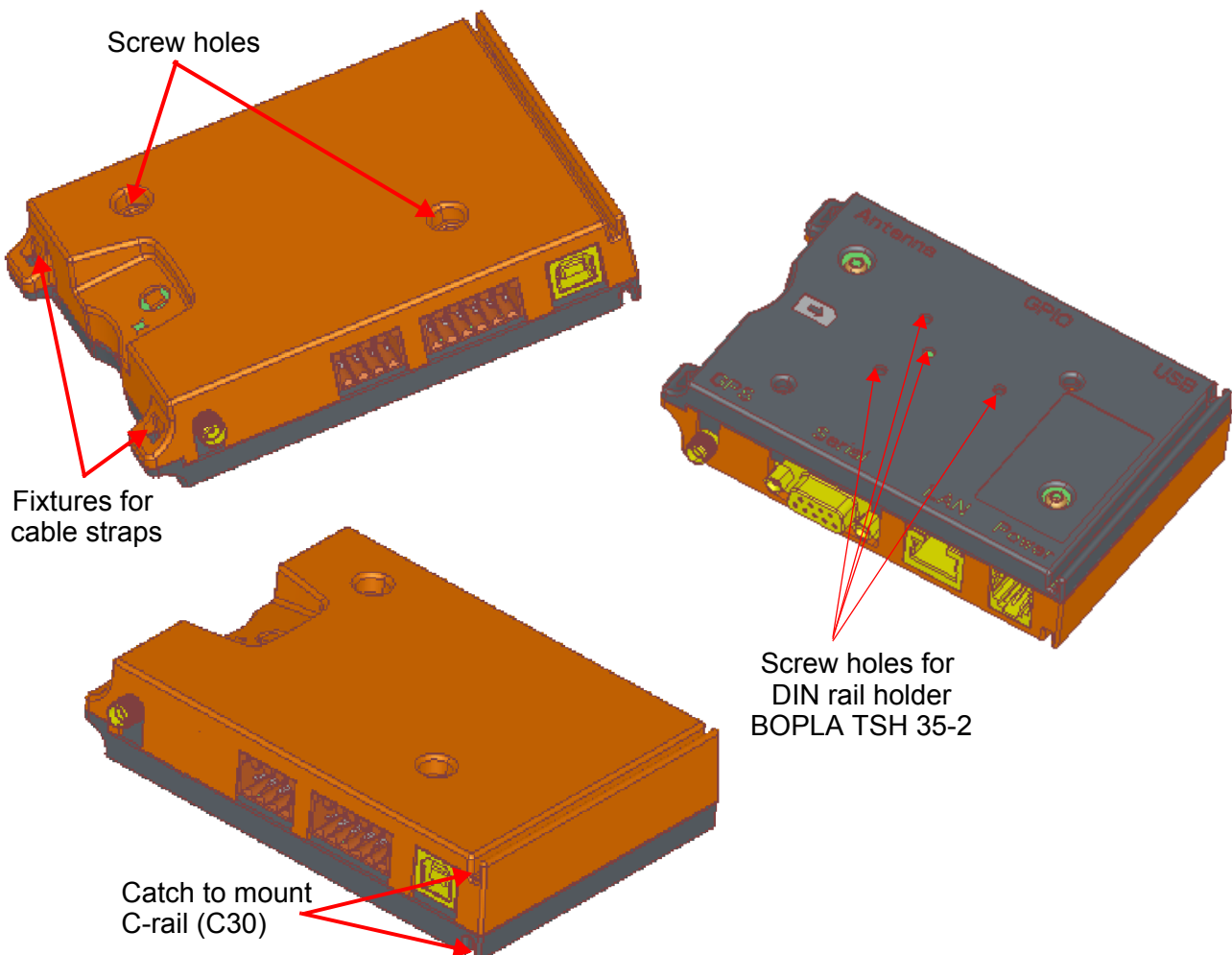
Figure 15: Java Terminal mechanical dimensions

## 5.2 Mounting the Java Terminal

There are a number of ways to mount the Java Terminal:

- Java Terminal can be attached to a rail installation or other surface using the two provided screw holes for screws, e.g., size M3.
- Java Terminal can be fastened to a rack or holding using the two provided fixtures for cable straps.
- Java Terminal can be slid onto a specific DIN rail made according to DIN EN 60715 - C section, C30 format. A catch at the terminal's bottom side will have to be removed to slide multiple terminals onto a single rail.
- Using a BOPLA TSH 35-2 universal DIN rail holder the Java Terminal can be fitted onto another special type of DIN rail made according to DIN EN 60715 - Top hat section, 35mm (e.g., Wago 210-113 steel carrier rail).

The following figure shows the various possibilities provided to mount the Java Terminal.



**Figure 16:** Mounting the Java Terminal

The various ways to mount the Java Terminal may be combined where appropriate. It is for example possible to slide the terminal onto a DIN rail and in addition use cable straps to fasten it to a holding.

## **5.3 Packaging**

Java Terminal come in terminal boxes:

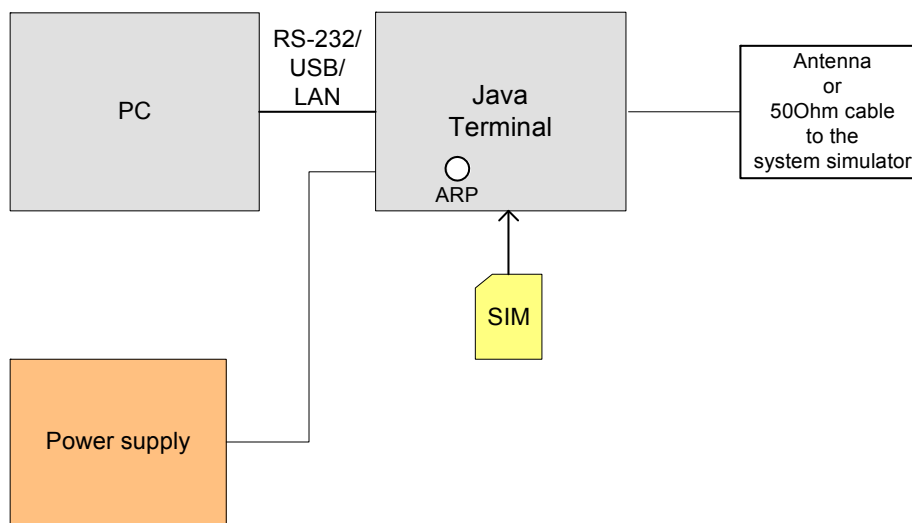
- Terminal box size: 191mm x 143mm x 44mm.

## 6 Full Type Approval

### 6.1 Thales Reference Setup

The Thales reference setup submitted to type approve Java Terminal consists of the following components:

- Java Terminal with approved Java module
- PC as MMI
- Power Supply



**Figure 17:** Reference equipment for approval

For ordering information please refer to [Chapter 7](#).



## 6.2 Restrictions

Later enhancements and modifications beyond the certified configuration require extra approvals. Each supplementary approval process includes submittal of the technical documentation as well as testing of the changes made.

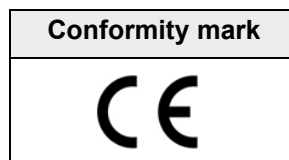
- No further approvals are required for customer applications that comply with the approved Java Terminal configuration.
- Extra approval must be obtained for applications using other accessories than those included in the approved Java Terminal configuration (power supply, MMI implementation supported by AT commands).

## 6.3 CE Conformity

The Java Terminal meet the requirements of the EU directives listed below:

- RED Directive 2014/53/EU

The Java Terminal are marked with the CE conformity mark (without a notified body number):



## 6.4 EMC

The Java Terminal comply with the equipment requirements specified in EN 301489-1, and -52 are covered by the RED Directive. For details see [Section 1.3](#).

## 6.5 Compliance with FCC and ISED Rules and Regulations

As an integrated product, the Java Terminals PLS62T-W USB and PLS62T-W LAN are fully compliant with the grant of the FCC Equipment Authorization and the ISED Certificates issued for the built-in Java modules, and therefore, bear the labels “Contains FCC ID: QIPPLS62W”, and accordingly “Contains IC: 7830A-PLS62W”.

The Equipment Authorization Certification for the Cinterion® Java modules is listed under the following identifiers:

*FCC Identifier: QIPPLS62W*

*ISED Certification Number: 7830A-PLS62W*

*Granted to THALES DIS AIS Deutschland GmbH*

### Notes (FCC):

Radiofrequency radiation exposure information:

This equipment complies with FCC radiation exposure limits set forth for an uncontrolled environment. This equipment should be installed and operated with minimum distance of 20 cm between the radiator and your body. This transmitter must not be co-located or operating in conjunction with any other antenna or transmitter.

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

This terminal equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

Changes or modifications made to this equipment not expressly approved by Thales may void the FCC authorization to operate this equipment.

This device contains UMTS, GSM and GPRS class functions in the 900, 1800 and 2100MHz bands that are not operational in U.S. Territories. This device is to be used only for mobile and fixed applications.

Users and installers must be provided with antenna installation instructions and transmitter operating conditions for satisfying RF exposure compliance: For more information on the RF antenna interface please refer to [Section 3.16](#) and [Section 4.5](#).

Please contact us for more information on the FCC Compliance details mentioned above:

Thales  
Edgar Canizales  
Arboretum Plaza II  
9442 Capital of Texas Highway, Suite 400  
Austin, TX 78759  
Tel: +1 512 215 7326  
Email: edgar.canizales@thalesgroup.com

**Notes (ISED):**

(EN) This Class B digital apparatus complies with Canadian ICES-003 and RSS-210. Operation is subject to the following two conditions: (1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

(FR) Cet appareil numérique de classe B est conforme aux normes canadiennes ICES-003 et RSS-210. Son fonctionnement est soumis aux deux conditions suivantes: (1) cet appareil ne doit pas causer d'interférence et (2) cet appareil doit accepter toute interférence, notamment les interférences qui peuvent affecter son fonctionnement.

**(EN) Radio frequency (RF) Exposure Information**

The radiated output power of the Wireless Device is below the Innovation, Science and Economic Development Canada (ISED) radio frequency exposure limits. The Wireless Device should be used in such a manner such that the potential for human contact during normal operation is minimized.

This device has also been evaluated and shown compliant with the ISED RF Exposure limits under mobile exposure conditions. (antennas are greater than 20cm from a person's body).

**(FR) Informations concernant l'exposition aux fréquences radio (RF)**

La puissance de sortie émise par l'appareil de sans fil est inférieure à la limite d'exposition aux fréquences radio d'Innovation, Sciences et Développement économique Canada (ISDE). Utilisez l'appareil de sans fil de façon à minimiser les contacts humains lors du fonctionnement normal.

Ce périphérique a également été évalué et démontré conforme aux limites d'exposition aux RF d'ISED dans des conditions d'exposition à des appareils mobiles (les antennes se situent à moins de 20cm du corps d'une personne).

## 7 List of Parts and Accessories

**Table 26:** List of parts and accessories



Description	Supplier	Ordering information
Java Terminal	Thales	Ordering number PLS62T-W USB: L30960-N2770-A100 PLS62T-W LAN: L30960-N2771-A100
Power supply unit Input: 100-240V / 50/60Hz / 500mA Output: 12V / 1,25A 	Thales	Power supply including 4 exchangeable AC plugs (EU, UK, US and AUS)  Ordering number: L36880-N8490-A82
DIN rail holder - BOPLA TSH 35-2 	BOPLA	Ordering number: 20035000 BOPLA Gehäuse Systeme GmbH Borsigstr. 17-25 D-32257 Bünde Phone: +49 (0)5223 / 969 - 0 Fax: +49 (0)5223 / 969 - 100 Email: <a href="mailto:info@bopla.de">info@bopla.de</a> Web: <a href="http://www.bopla.de">http://www.bopla.de</a>
LTE Broadband Antenna fitting to the used frequency bands, 50 Ohm, SMA connector (male)		
Antenna - Panorama LPBEM-7-27 covering frequencies 698-960 MHz and 1710-3800 MHz, 2m RG174, SMA(m)	Panorama Antennas Ltd	LPBEM-7-27-2SP Panorama Antennas Ltd Frogmore London SW18 1HF United Kingdom Phone: +44 (0)20 8877 4444 Email: <a href="mailto:sales@panorama-antennas.com">sales@panorama-antennas.com</a> Web: <a href="http://www.panorama-antennas.com">www.panorama-antennas.com</a>
RS-232 cable with 9-pin D-sub connector (male)	Tecline	Tecline GmbH Behrener Straße 8 D-66117 Saarbrücken Phone: +49-681-926-78-70 Fax: +49-681-926-78-555 Web: <a href="http://www.tecline-edv.de/">http://www.tecline-edv.de/</a>

Table 26: List of parts and accessories

Description	Supplier	Ordering information
<p>8-pin and 12-pin header GPIO connector (female plug) for GPIO interface Type: 8-pin: B2CF 3.50/08/180 SN BK BX 12-pin: B2CF 3.50/12/180 SN BK BX</p> 	<p>Weidmueller</p>	<p>Ordering number (12-pin): 1277510000 Ordering number (8-pin): 1277480000 Weidmüller Interface GmbH &amp; Co. KG Klingenbergstraße 16 D-32758 Detmold</p> <p>Phone: +49 5231 14-0 Fax: +49 5231 14-2083 Email: <a href="mailto:info@weidmueller.de">info@weidmueller.de</a> Web: <a href="http://www.weidmueller.com">http://www.weidmueller.com</a></p>

## 8 Appendix A: (Hardware) Watchdog

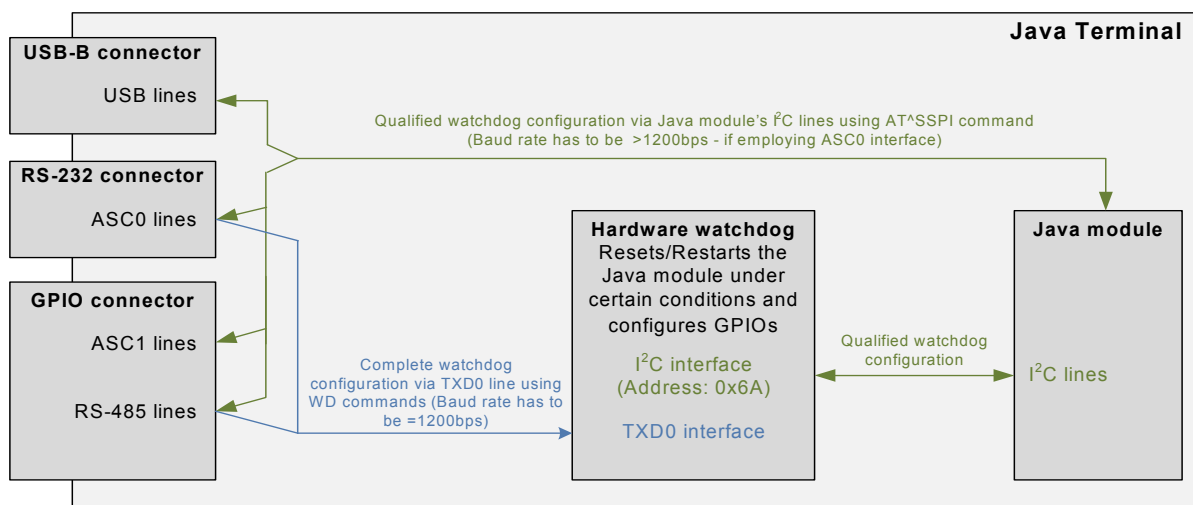
The watchdog is part of the Java Terminal and connected to the Java module itself (see also [Figure 4](#)). It can be used to

- Safely reset the module in certain conditions
- Restart the module when it has turned off
- Configure level shifter's GPIO directions and DSR0/ADC1\_IN availability at the GPIO connector

The complete watchdog functionality can be configured by means of special watchdog configuration commands (i.e., WD commands) that are passed to the watchdog via the serial TXD0 line. For details on how to configure the complete watchdog functionality see [Section 8.3](#).

A qualified set of configuration commands - level shifter's GPIO directions, DSR0/ADC1\_IN usage, trigger delay and firmware infos - can be specified by means of a special write/read protocol handled by the watchdog's I<sup>2</sup>C interface. For details on how to configure the watchdog via I<sup>2</sup>C interface see [Section 8.4](#).

The below [Figure 18](#) illustrates the various ways the watchdog may be accessed and configured.



**Figure 18:** Hardware watchdog configuration

Note: Unless the complete configuration scope is required, the qualified watchdog configuration via I<sup>2</sup>C interface should be used.

## 8.1 Reset Conditions

The watchdog implements three conditions, under which a reset of the module is automatically performed:

- Repetitive: A module reset is performed frequently and repetitive. This reset condition can be used to force the module to reconnect to the mobile network once in a while. Typical frequencies can be 24h or longer. This feature can be configured via the RST\_REP timeout.
- UART activity: The watchdog can be used to reset the module, when no response from the module on AT commands is recognized for a specified amount of time. To prevent the reset, the module has to be active frequently on the UART interface. This reset condition can be configured via the RST\_UART timeout, it is deactivated when timeout parameter = 0.
- GPIO activity: The watchdog can be used to reset the module, when no activity on the designated GPIO signal is recognized for a specified amount of time. To prevent the reset, the module has to be active frequently by toggling this GPIO signal. This reset condition can be configured via the RST\_GPIO timeout, it is deactivated when timeout parameter = 0.

When the watchdog is enabled, it will observe the activities on the UART and GPIO interfaces as well as the Java module status, depending on timeout parameter settings and perform frequent resets, if it is configured to do so.

### 8.1.1 Reset stages

Basically, there are up to two escalation stages during a module reset:

- First stage (regular fast shutdown): The watchdog shuts down the module via an internal fast shutdown signal. The fast shutdown procedure will then still finish any data activities on the Java module's flash file system, thus ensuring data integrity, but will no longer deregister gracefully from the network, thus saving the time required for network deregistration. Afterwards, i.e. after an internal signaling has gone low, the module is regularly restarted.
- Second stage (emergency restart): If the module can for some reasons not be switched off successfully during the first stage, the watchdog resets the module via an internal EMERG\_RST signal. The emergency restart procedure includes disconnecting the power supply lines, and causes the loss of all information stored in the Java module's volatile memory.

During the first stage the watchdog waits for up to three seconds for the internal signaling to go low. If the internal signaling does not change, the watchdog escalates to the second stage in order to switch off and restart the module.

The watchdog can also be configured to automatically switch on resp. power up the module following a shutdown and a configured delay time (always-on mode).

### 8.1.2 Reset Delay

The watchdog implements a protection mechanism to prevent too frequent module resets. When the delayed reset mechanism is enabled, the watchdog will start its activity only after the specified amount of time, MIN\_START\_TIME. A reasonable value for this timeout is 30 minutes. After the watchdog startup, after a module reset and also after enabling the watchdog, no reset of the module is performed before the timeout of MIN\_START\_TIME. The reset delay mechanism may be useful for software updates.

When the watchdog is enabled, resets can be prevented once for a certain amount of time. This timeout, TRG\_DEL, can be configured via the I<sup>2</sup>C interface. It can be particularly useful when a software update is performed. Using the TRG\_DEL timeout will prevent the watchdog from resetting the module during the running TRG\_DEL timeout, so that the update can be performed safely. An upcoming reset event will be shifted and catch up after the TRG\_DEL timeout.

## 8.2 Restart Conditions

When the watchdog is enabled, it will observe the modules on/off state. When it is configured to keep the module "always on", it will restart the module after the specified amount of time after it has discovered that the module has turned off. This important feature is useful in rough environments with often power losses and out-of-temperature conditions where it secures a safe module operation. The timeout condition for the restart feature is called ALWAYS\_ON.



## 8.3 Complete Watchdog Configuration

The complete hardware watchdog functionality can be configured via the TXD0 line of the serial interface ASC0 as described in this section.

The watchdog listens on the TXD0 line exclusively at the low baudrate 1200bps. This means that if using the watchdog this low baud rate is reserved and should not be configured for the module's asynchronous serial interface ASC0. The TXD0 line can be accessed either via RS-232 interface or via RS-485 interface (in conjunction with an RS232-to-RS485 adapter). Note that in this case the watchdog gives no feedback via an output line. Commands are acknowledged by green ON LED - see [Section 8.3.1](#).

So, to control and configure the watchdog, a terminal program MUST be set to 1200bps, before a command can be sent to the watchdog. See [Section 8.3.1](#) for the watchdog's configuration command specification. Once completed, the terminal program should be changed to higher baud rates again to enable proper communication with the module. Note that the Java module has to be up and running for this type of configuration.

Please note that a qualified set of configuration commands can also be configured via I<sup>2</sup>C interface. See [Section 8.4](#) for details.

### 8.3.1 Watchdog (WD) Command Specification

The general watchdog command syntax is as follows:

WD=<command>,<argument>,<checksum><NL>

Where

- <command> specifies the command name
- <argument> gives the numeric argument
- <checksum> is the sum of the digits of the argument. (e.g. the argument 124 produces a checksum 7, because  $1+2+4=7$ ).

Whenever a non-volatile command is executed, it is saved in the watchdog's flash memory. At watchdog start, the last state is loaded from flash memory.

If a config command was successfully executed by the watchdog, the green ON LED flashes two times. The watchdog commands are implemented as text commands. In case a command error occurs - e.g., a checksum failure - the green ON LED flashes 4 times.

## 8.3 Complete Watchdog Configuration

The following watchdog configuration commands are available:

- Watchdog on/off - see [Section 8.3.1.1](#)
- Test mode - see [Section 8.3.1.2](#)
- Repetitive module reset - see [Section 8.3.1.3](#)
- UART reset - see [Section 8.3.1.4](#)
- GPIO reset - see [Section 8.3.1.5](#)
- I<sup>2</sup>C reset - see [Section 8.3.1.6](#)
- Restart delay - see [Section 8.3.1.7](#)
- Always on - see [Section 8.3.1.8](#)
- Load default values - see [Section 8.3.1.9](#)
- Change the Watchdogs I<sup>2</sup>C Address - see [Section 8.3.1.10](#)
- Set GPIO Direction - see [Section 8.3.1.11](#)
- Configure ADC1\_IN/DSR0/SPI\_CLK Line - see [Section 8.3.1.12](#)

Note: Changing the watchdog configuration using any of the following commands disables the watchdog: Repetitive module reset, UART reset, GPIO reset, Restart delay and Always on. With these commands the new configuration setting becomes effective only after the hardware watchdog is enabled again.

### 8.3.1.1 Watchdog On/Off

Command	ON
Parameter	<on off>
Type	Boolean
Range	0: Off (watchdog disabled) 1: On (watchdog enabled)
Default	0: Off
Non-volatile	Yes
Example	WD=ON,0,0 // disables the watchdog WD=ON,1,1 // enables the watchdog

This command is used to enable or disable the watchdog function. When disabled, all timers are stopped and the watchdog doesn't perform a module reset. When enabled, all configured timers start after a delay time of MIN\_START\_TIME. If MIN\_START\_TIME=0, all reset timers start immediately. Also, when the watchdog is enabled and ALWAYS\_ON>0, the watchdog observes the modules on/off state, and starts the module in case it detects that the module is off.

### 8.3.1.2 Test Mode

Command	TEST_MODE
Parameter	<on off>
Type	Boolean
Range	0: Off (Exit test mode) 1: On (Enter test mode)
Default	0: Off
Non-volatile	Yes
Example	WD=TEST_MODE,0,0 // Exit test mode WD=TEST_MODE,1,1 // Enter test mode

This commands configures the watchdog's test mode. In test mode the watchdog operates normally, but does not actually perform a module reset. Instead, it signals the (simulated) reset via the LED by flashing the green ON LED two times to visualize the watchdog trigger. Entering the test mode disables the actual watchdog functionality.

### 8.3.1.3 Repetitive Module Reset

Command	RST_REP
Parameter	<timeout>
Type	Milliseconds
Range	0 .. $2^{32}-1$
Default	0: Feature is disabled
Non-volatile	Yes
Example	WD=RST_REP,1800000,9 // Reset every 30 minutes

This command configures a repetitive module resets, if the watchdog is enabled. The parameter sets the RST\_REP timeout value. If the watchdog is enabled, an unconditional module reset every RST\_REP milliseconds is performed.

Changing this configuration disables the watchdog. The feature becomes active, if the watchdog is enabled again, and after the MIN\_START\_TIME has passed.

For normal operation, this value should be set to a value greater than 30 minutes, some use cases may even require a setting > 24h.

## 8.3 Complete Watchdog Configuration

**8.3.1.4 UART Reset**

Command	RST_UART
Parameter	<timeout>
Type	Milliseconds
Range	0 .. $2^{32}-1$
Default	0: Feature is disabled
Non-volatile	Yes
Example	WD=RST_UART,600000,6 // Resets the module if there was no activity on the RXD0 line for 10 minutes

This command configures a module reset, if no UART activity from the module was observed for the specified amount of time - RST\_UART. The module has to be active on the RXD0 signal within the specified time period; otherwise the watchdog will reset the module.

Changing this configuration disables the watchdog. The feature becomes active, if the watchdog is enabled again, and after the MIN\_START\_TIME has passed.

For normal operation, this value should be set to a value greater than 10 minutes (600000).

**8.3.1.5 GPIO Reset**

Command	RST_GPIO
Parameter	<timeout>
Type	Milliseconds
Range	0 .. $2^{32}-1$
Default	0: Feature is disabled
Non-volatile	Yes
Example	WD=RST_GPIO,600000,6 // Resets the module if there was no activity on the WD_RETRIG line for 10 minutes

This command configures a module reset, if no activity from the module was observed on the signal WD\_RETRIG for the specified amount of time (see [Figure 4](#) for WD\_RETRIG (GPIO8) line). The module has activate the WD\_RETRIG signal by toggling the GPIO8 module output within the specified time period. Otherwise the watchdog will reset the module. If enabled, each GPIO8 toggling resets the timer to its configured value. Note that for this feature to work, GPIO8 has to be available and enabled, and its direction must be set to output. Please refer to [Section 8.3.1.11](#), [Section 8.4](#) and [Section 8.5](#) for details on how to configure GPIO directions. If configured as WD\_RETRIG signal, GPIO8 cannot be used for any other purpose.

Changing this configuration disables the watchdog. The feature becomes active, if the watchdog is enabled again, and after the MIN\_START\_TIME has passed.

For normal operation, this value should be set to a value greater than 10 minutes (600000).

### 8.3.1.6 I<sup>2</sup>C Reset

Command	RST_I2C
Parameter	<timeout>
Type	Milliseconds
Range	0 .. 2 <sup>32</sup> -1
Default	0: Feature is disabled
Non-volatile	Yes
Example	WD=RST_I2C,600000,6 // Resets the module if there was no keep alive signal received at the I <sup>2</sup> C bus address 0x09 for 10 minutes.

This command configures an Ethernet sub-system reset, if no activity from the Ethernet sub-system or a possible external application also connected to the I<sup>2</sup>C bus was observed for the specified amount of time (see [Figure 4](#) and [Section 3.8](#) for I<sup>2</sup>C lines). If enabled, the Ethernet sub-system or an external application will have to send keep alive signals to the I<sup>2</sup>C bus address 0x09 within the specified time period. If the keep alive signals are not regularly received at this address, the watchdog will reset the Ethernet sub-system.

Changing this configuration disables the watchdog. The feature becomes active, if the watchdog is enabled again, and after the MIN\_START\_TIME has passed.

For normal operation, this value should be set to a value greater than 10 minutes (600000).

### 8.3.1.7 Restart Delay

Command	MIN_START_TIME
Parameter	<timeout>
Type	Milliseconds
Range	0 .. $2^{32}-1$
Default	18000000ms (30 minutes)
Non-volatile	Yes
Example	WD=MIN_START_TIME,18000000,9 // Prevents module resets for 30 minutes after each module startup, and after the watchdog becomes active

This command configures the MIN\_START\_TIME timeout value. By setting the MIN\_START\_TIME, the watchdog no longer performs a module reset for the given amount of time, after module startup. Whenever the module has been reset and restarted, as well as after the watchdog has been enabled, the watchdog will wait for MIN\_START\_TIME before performing any (further) resets. The watchdog's reset timer only starts after the MIN\_START\_TIME has expired.

Changing this configuration disables the watchdog. The feature becomes active, if the watchdog is enabled again.

It is strongly recommended to set this value to a time period of more than 30 minutes for normal operation. During development it may be set to values of less than 30 minutes, but should always be greater than the time the module needs for a complete start up including Java. Also, a Java module firmware or userware update right after startup should be taken into account which may take up to 15 min.

## 8.3 Complete Watchdog Configuration

**8.3.1.8 Always On**

Command	ALWAYS_ON
Parameter	<timeout>
Type	Milliseconds
Range	0 .. $2^{32}-1$
Default	0: Feature is disabled
Non-volatile	Yes
Example	WD= ALWAYS_ON,60000,6 // Observes the module and restarts it 60 seconds after it has been turned off

This command configures the on/off-state observation of the module by specifying a timeout value for ALWAYS\_ON. If enabled, the watchdog observes the module's internal signalling. If the watchdog detects that the module is OFF, it will restart the module after the timeout of ALWAYS\_ON milliseconds.

Changing this configuration disables the watchdog. The feature becomes active, if the watchdog is enabled.

It is strongly recommended to set this value to a time period of more than 1 minute for normal operation to avoid oscillation in e.g. out-of-temperature events. In case of over/under temperature shut down it can be expected that the temperature does not change significantly within a minute. During development the timeout may be set to a period of less than 1 minute.

**8.3.1.9 Load Default Values**

Command	DFT_VAL
Parameter	<1>
Type	Fixed
Range	1
Default	---
Non-volatile	No
Example	WD= DFT_VAL,1,1 // Loads the default values

This command loads the default configuration values. This disables the watchdog. If the watchdog is enabled, the reset timeout values, the MIN\_START\_TIME timeout and the ALWAYS\_ON timeout become active. Other configuration values become active immediately. The loaded default values are also persistent, i.e. written to the flash memory.

### 8.3.1.10 Change the Watchdog's I<sup>2</sup>C Address

Command	I2C_ADDR
Parameter	<address>
Type	Number
Range	1-127
Default	106 (0x6A)
Non-volatile	Yes
Example	WD= I2C_ADDR,87,15 // Changes the I <sup>2</sup> C address to 87d (0x57)

The watchdog's I<sup>2</sup>C slave address can be changed to any 7-bit address. This may become necessary to avoid address conflicts on the I<sup>2</sup>C bus, if used in an environment, where the default I<sup>2</sup>C address "0x6A" is already in use by other slave devices connected to the Java Terminal.

Changing the I<sup>2</sup>C address takes effect immediately and has no impact on the watchdog's enabled/disabled state.



## 8.3 Complete Watchdog Configuration

**8.3.1.11 Set GPIO Direction**

Command	GPIO_DIR	
Parameter	<pin-config>	
Type	Number	
Range	0-1023	
Default	0 (0x000, 0000000000b)	
Non-volatile	Yes	
Example	WD= GPIO_DIR,682,16 // Sets the GPIOs alternating to output and input (binary value: 1010101010b)	

This command configures the input/output direction of level-shifters to the module's externally available GPIO pins. The argument is a 10-bit number, representing the 10 adjustable directions of the GPIO level-shifters. A set bit (value 1) sets the respective level-shifter to the output direction. A cleared bit changes the respective level-shifter to input direction. The following table describes the connection between the 10-bit argument number, the modules GPIO pins, and the Java Terminal GPIO connectors 8-pin and 12-pin:

10-Bit number	GPIO	8-pin connector	12-pin connector	Default
0	GPIO6	1	-	Input
1	GPIO7	2	-	Input
2	GPIO8	3	-	Input
3	GPIO11	4	-	Input
4	GPIO12	5	-	Input
5	GPIO13	6	-	Input
6	GPIO22	7	-	Input
7	GPIO23	8	-	Input
8	GPIO21	-	12	Input
9	GPIO20	-	11	Input

Changing the directions of the level-shifters must be executed with great care. They may only be set in accordance with the Java modules' GPIO input/output configuration. Special care must be taken that no outputs are cross-connected during the switching phase.

Configuring a Java Terminal output, the level shifter output should be set first, followed by the module output configuration. Configuring an input, the module input should be set first, followed by the level shifter input. Please refer to [Section 8.5](#) for more information on how to configure GPIO directions.

Please note that the GPIO direction can also be configured via I<sup>2</sup>C interface (see [Section 8.4](#)). It is recommended to use the I<sup>2</sup>C interface to configure the GPIO direction.

Note: Not every GPIO is supported by every Java Terminal variant - see [Section 3.8](#).

### 8.3.1.12 Configure ADC1\_IN/DSR0/SPI\_CLK Line

Command	ADC_DSR0	
Parameter	<input/output>	
Type	Boolean	
Range	0: Analog input (ADC1_IN) 1: Digital output (DSR0/SPI_CLK)	
Default	0: Analog input (ADC1_IN)	
Non-volatile	Yes	
Example	WD= ADC_DSR0,0,0 // Configures the line to be analog input WD= ADC_DSR0,1,1 // Configures the line to be digital output	

This command configures the the ADC/DSR0/SPI\_CLK signal on the GPIO connector to be either an analog input line (ADC) or a digital output line (DSR0/SPI\_CLK). If configured as analog input, the signal is connected to the Java module's ADC1\_IN line. If configured as digital output, the signal is connected to the Java module's DSR0/SPI\_CLK line that can be configured to be either DSR0 or SPI\_CLK.

Note: If configuring the ADC1\_IN/DSR0/SPI\_CLK line please take great care to be in accordance with the Java module's current configuration of the ADC1\_IN and DSR0/SPI\_CLK signals.

## 8.4 Qualified Watchdog Configuration

While the complete watchdog functionality may be configured via TXD0 line of the ASC0 interface (for details see [Section 8.3](#)), a qualified set of configuration commands can also be specified via the watchdog's I<sup>2</sup>C interface as described in this section. The Java module has to be up and running for this type of configuration.

The watchdog's I<sup>2</sup>C interface is accessible by means of the Java module's I<sup>2</sup>C lines, and either via the Java module's AT command interface, or through a Java MIDlet during runtime (see [Figure 18](#)). **Note:** When using a Java MIDlet to set GPIO directions using the I<sup>2</sup>C interface, please allow for a guard time of at least 500ms between two I<sup>2</sup>C commands.

The I<sup>2</sup>C interface implements the write and the read protocol as described in [Section 8.4.1](#). The 7-bit device address is 0x6A (binary: 1101010). The default address can be changed by configuration command (see [Section 8.3.1.10](#)).

### 8.4.1 I<sup>2</sup>C Interface Command Specification

#### 8.4.1.1 WRITE Command Syntax

S	Slave address (including write bit "W")	A	Register address	A	Data byte	A	P
---	--	---	------------------	---	-----------	---	---

Example setting the GPIO12 signal direction to "output" (see also section [Examples](#)):

S	0xD4 (including write bit "0")	A	0x14	A	0x01	A	P
---	-----------------------------------	---	------	---	------	---	---

Legend:

S: Start Condition, W: Write bit (=0), A: Acknowledge, P: Stop Condition.

#### 8.4.1.2 READ Command Syntax

S	Slave address (including read bit "R")	A	Register address	A	Data length (only one byte)	N	P
---	---	---	------------------	---	--------------------------------	---	---

Example reading the last status = OK (see also section [Examples](#)):

S	0xD5 (including read bit "1")	A	0x00	A	0x01 (only one byte)	N	P
---	----------------------------------	---	------	---	-------------------------	---	---

Legend:

S: Start Condition, R: Read bit (=1), A: Acknowledge, N: Not Acknowledge, P: Stop Condition.

### 8.4.1.3 I<sup>2</sup>C Protocol Overview

In write mode (i.e., slave address “0xD4”), one address byte and one data byte is sent to the Java Terminal/Watchdog. The address byte specifies a register to write the data byte to. The data byte value is only written, if it is valid, i.e., in the specified range. After a write attempt, the status code of the operation is saved and the read address register (RAR) is automatically set to the status register address (SR). A subsequent read command from the status register (SR) will then return the latest status code (see [Table 28](#)). Only when the address byte is the RAR, i.e. another register is selected to be read, the RAR is not automatically set to the SR register. See [Section 8.4.1.4](#) for sample watchdog configurations via I<sup>2</sup>C.

In read mode, one data byte can be read from the Java Terminal/Watchdog. Attempts to read more bytes will result in undefined values being returned by the device. The device will always return the value that is addressed by the RAR. To read a specific register, a write command with RAR as the address byte and the register to be read as the data byte has to be issued first. The next read will then return the value at this address. Note that there are only a few registers that can be read (see register table - [Table 27](#)). When the RAR is written with a non-read address, the RAR is set to the SR, and the status code `ILLEGAL_ARGUMENT` is saved. Note also that a consecutive read is not valid, as the return value will be `ILLEGAL_ARGUMENT`, but the caller cannot determine whether the result is the value at the faulty address or an error status code. See [Section 8.4.1.4](#) for sample watchdog configurations via I<sup>2</sup>C.

### 8.4.1.4 I<sup>2</sup>C Commands

The following table lists the address register for configuration commands via I<sup>2</sup>C interface.

**Table 27:** Address register for I<sup>2</sup>C commands

Register address	Read/Write	Description	Name	Non-volatile	Default	Value range
0x00	R	Status; only address register to read directly from.	SR	-	OK	See result codes <a href="#">Table 28</a>
0x09	W	RST_I2C		Yes	0	
0x10	W	GPIO6	GPIOxR	Yes	0	Level shifter's GPIO direction: 0: Input 1: Output
0x11	W	GPIO7		Yes	0	
0x12	W	GPIO8		Yes	0	
0x13	W	GPIO11		Yes	0	
0x14	W	GPIO12		Yes	0	
0x15	W	GPIO13		Yes	0	
0x16	W	GPIO22		Yes	0	
0x17	W	GPIO23		Yes	0	
0x18	W	GPIO21		Yes	0	
0x19	W	GPIO20		Yes	0	
0x30	R	GPIO direction Low Byte: Read out 8 bits for the GPIOs [15,14,13,12,11,8,7,6]	GPIO_LBR	-		[0..0xFF]

## 8.4 Qualified Watchdog Configuration

**Table 27:** Address register for I<sup>2</sup>C commands

Register address	Read/Write	Description	Name	Non-volatile	Default	Value range
0x31	R	GPIO direction High Byte: Read out 2 bits for the GPIOs 20 and 21 in the representation: [0,0,0,0,0,0,<20>,<21>]	GPIOHBR	-		[0..0xFF]
0x50	R/W	ADC1_IN/DSR0	ADCDSRR	Yes	0x00	0: Analog In 1: Digital Out
0x80	W	Trigger delay. Specifies delay time for a reset. If a trigger delay time is specified, the watchdog is prevented from resetting the module for the given time.	TDR	No	0x00	Set time in minutes. 1...255: Minutes 0: Disable
0xFD	R	Hardware watchdog's firmware version	VER	--		[0x00..0x99] [MAJ MIN] 4:MSB: MAJ 4:LSB: MIN  MAJ: Main release number (e.g., 1.x) MIN: Sub release number (e.g., x.0) as in version v1.0
0xFF	W	Read address register (RAR)	RAR	No	0x00	0x00..0xFF Only valid addresses contain valid values

Possible result codes for status command (see [Section 8.4.1.3](#) and above [Table 27](#)):

**Table 28:** I<sup>2</sup>C status result codes

Result	Code	Comment
OK	0x00	Last command was executed successfully
PROTOCOL_ERROR	0x01	Protocol error, i.e. wrong number of bytes
ILLEGAL_ADDRESS	0x02	Illegal register address
ILLEGAL_ARGUMENT	0x03	Illegal argument. Argument is out of allowed range.
UNDEFINED	0xFF	

**Note:** When using Java to set GPIO directions over the I<sup>2</sup>C interface, please allow for a guard time of at least 500ms between I<sup>2</sup>C commands.

## Examples

The following two samples show how the watchdog can be configured by means of the watchdog's I<sup>2</sup>C interface and using the AT^SSPI command over RS-232/ASC0 to transfer the I<sup>2</sup>C user data. Please refer to [1] for more information on the AT command AT^SSPI and on how to configure and control the data transfer over the Java module's I<sup>2</sup>C interface.

The above Table 27 specifies the address register that can be used in I<sup>2</sup>C configuration commands.

The **first** example sets the level shifter's GPIO12 direction to "output". It therefore configures a write register marked as "W" in Table 27. For more information on the configuration of GPIO directions see Section 8.5.

AT^SSPI=	Open the Java Terminal I <sup>2</sup> C data connection.
CONNECT	Indicates that the connection is open.
<aD41401>	WRITE command enclosed by <>: "a" is a command ID to better identify and match acknowledgments, "D4" indicates the slave address (write mode), "14" specifies the address register GPIO12, and "01" sets the data byte (i.e., line is "output"). <b>Note:</b> The data byte value is only written if valid, i.e., if in the specified range. After a WRITE command, the status code of the operation is saved to the status register (SR) and a subsequent READ command from the status register will then return the latest status code as listed in Table 28.
{a+}	Acknowledgement enclosed in curly brackets of a successful data transmission.
<bD50001>	READ command enclosed by <>: "b" is a command ID to better identify and match acknowledgments, "D5" indicates the slave address (read mode), "00" specifies the address register SR, and "01" sets the data length to be read. <b>Note:</b> The READ command can only be called in conjunction with the SR address "00" and the data length of one byte "01".
{b+00}	Acknowledgement enclosed in curly brackets of a successful data transmission, together with the response code "00" indicating that the command was successfully executed.
#	Close data connection.
OK	Connection closed.

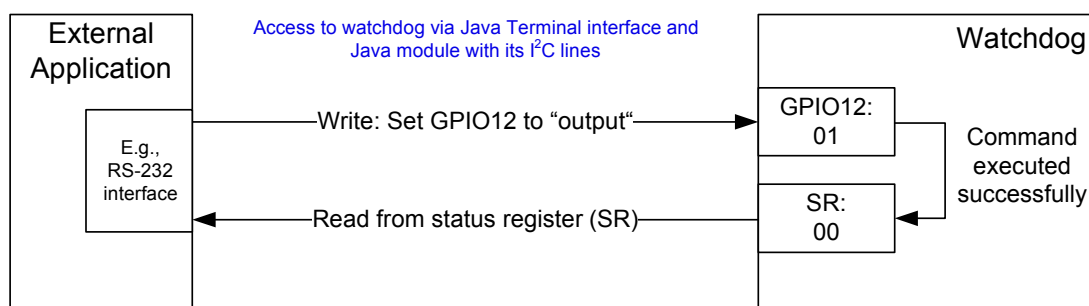


Figure 19: Write data to address register

8.4 Qualified Watchdog Configuration

The **second** example listed below reads out the hardware watchdog’s firmware version, it therefore uses a read register marked as “R” in [Table 27](#).

However, except for the status address register (SR) no information can be directly retrived from an address register itself, but only indirectly by means of a so-called read-address-register (RAR). An initial WRITE command has to link the register to be read to the RAR first. Now the RAR is linked to the register to be read, and the content of this register can be read from the SR.

AT^SSPI=	Open the Java Terminal I <sup>2</sup> C data connection.
CONNECT	Indicates that the connection is open.
<aD4FFFD>	WRITE command enclosed by <>: “a” is a command ID to better identify and match acknowledgments, “D4” indicates the slave address (write mode), “FF” specifies the read address register RAR, and “FD” sets the data byte to the watchdogs firmware version register VER (i.e., RAR and VER are linked by this command). <b>Note:</b> The data byte value is only written if valid, i.e., if in the specified range. After a WRITE command, the status code of the operation, in this case, i.e., where the register address is the RAR, the content of the register given as data byte is saved to the status register (SR) and a subsequent READ command from the status register will then return the register value, i.e., the hardware watchdog’s firmware version.
{a+}	Acknowledgement enclosed in curly brackets of a successful data transmission.
<bD50001>	READ command enclosed by <>: “b” is a command ID to better identify and match acknowledgments, “D5” indicates the slave address (read mode), “00” specifies the address register SR, and “01” sets the data length to be read. <b>Note:</b> The READ command can only be called in conjunction with the SR address “00” and the data length of one byte “01”.
{b+10}	Acknowledgement enclosed in curly brackets of a successful data transmission, together with the response code “10” indicating that the command was successfully executed. The resposne code gives the watchdog’s firmware version as v1.0.
#	Close data connection.
OK	Connection closed.

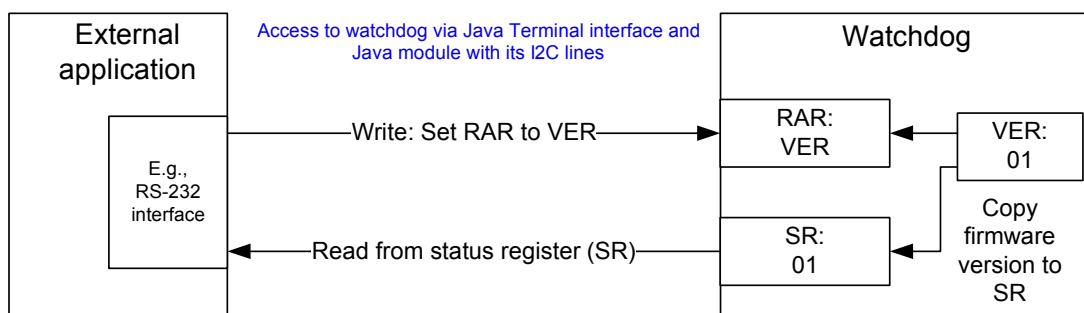


Figure 20: Read data from address register

## 8.5 Configuring GPIO Directions

As already shown in [Figure 4](#) and mentioned in [Section 8.3.1.11](#) the GPIO pins at the Java Terminal' GPIO connector are not electrically identical to the GPIO lines at the integrated Java module. A built-in level shifter needs to ensure that the voltage level is correctly converted.

Because of that it is necessary to not only configure the Java modules' GPIO settings by AT command, but to also configure the the built-in level shifter's GPIO directions by means of the watchdog.

As described in [Section 8.3](#), [Section 8.4](#) and shown in [Figure 18](#) there are various ways to configure the watchdog and therefore the level shifter's GPIO directions:

- A qualified configuration including GPIO directions can be done using the watchdog's I<sup>2</sup>C interface. For details see [Section 8.5.1](#). This is the recommended method to set the level shifter's GPIO direction.
- A complete configuration can be done using the TXD0 line of the ASC0 interface. For details see [Section 8.5.2](#).

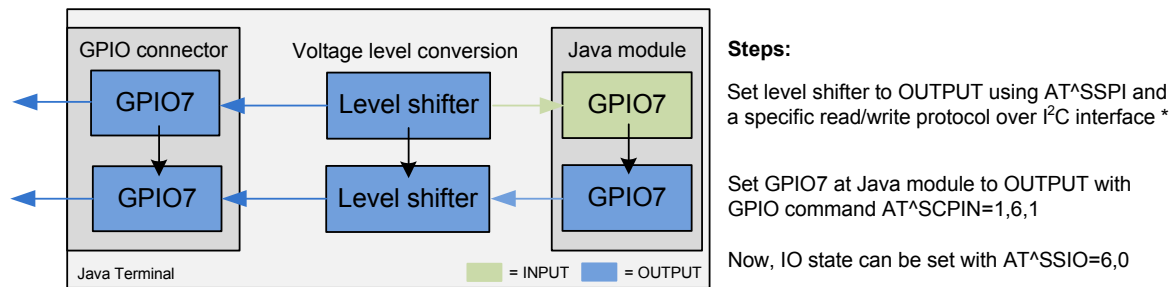
Notes: The VCCref signal at the Java Terminal's GPIO connector needs to be connected - for instance to the +5Vout signal - for the level shifters to be active. Also note that in order to have a defined level for GPIOs configured as inputs, external pull-ups or pull-downs will have to be implemented at the GPIO lines.



### 8.5.1 Configuration using I<sup>2</sup>C Interface

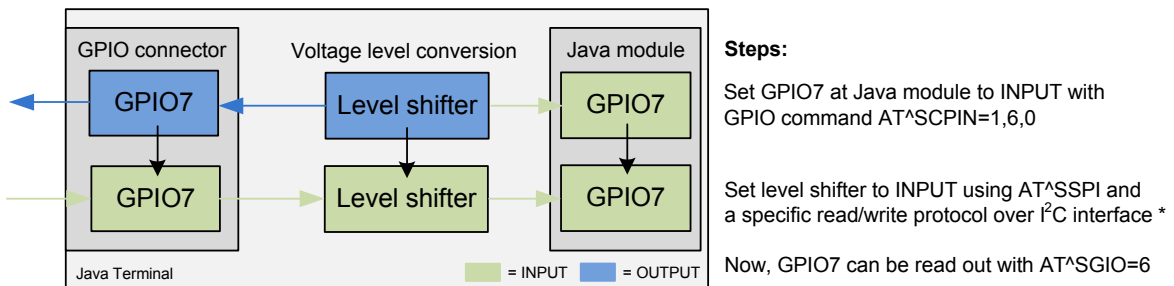
Please refer to [Section 8.4](#) for more information on how to configure the watchdog via I<sup>2</sup>C interface.

[Figure 21](#) explains the configuration steps required to set the Java Terminal's GPIO7 signal direction to OUTPUT (default direction is INPUT):



**Figure 21:** Setting GPIO direction to OUTPUT via I<sup>2</sup>C interface

[Figure 22](#) shows the configuration steps required to re-set the Java Terminal's GPIO7 signal direction to INPUT:



**Figure 22:** Setting GPIO direction to INPUT via I<sup>2</sup>C interface

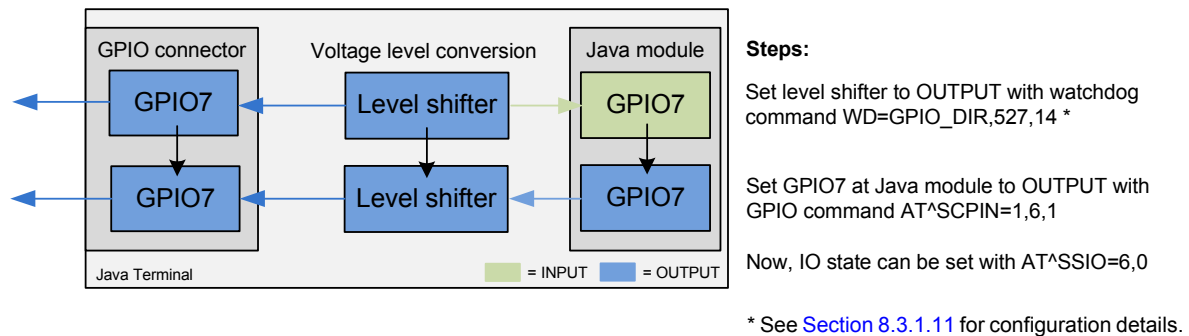
For more information on the AT commands mentioned in the above figures see also [\[1\]](#).

**Note:** When using a Java MIDlet to set GPIO directions using the I<sup>2</sup>C interface, please allow for a guard time of at least 500ms between two I<sup>2</sup>C commands.

### 8.5.2 Configuration using TXD0 Line of ASC0 Interface

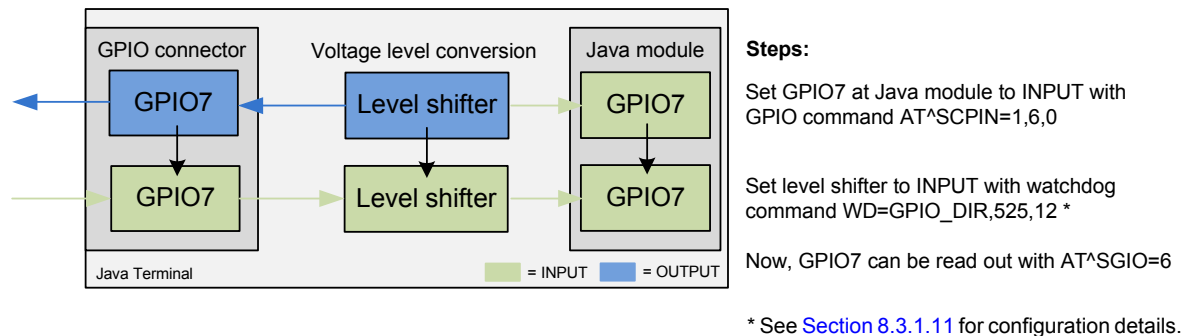
Please refer to [Section 8.3](#) for more information on how to configure the watchdog via TXD0 line of ASC0 interface.

[Figure 21](#) explains the configuration steps required to set the Java Terminal's GPIO7 signal direction to OUTPUT (default direction is INPUT):



**Figure 23:** Setting GPIO direction to OUTPUT via TXD0 line (at 1200bps)

[Figure 21](#) shows the configuration steps required to re-set the Java Terminal's GPIO7 signal direction to INPUT:



**Figure 24:** Setting GPIO direction to INPUT via TXD0 line (at 1200bps)

For more information on the AT commands mentioned in the above figures see also [\[1\]](#).

## 9 Appendix B: Ethernet Setup and Usage

Once LTE Terminal is connected and set up as described in [Section 9.1](#), the Ethernet connection can be used to transparently exchange data.

Via Ethernet link it is also possible to access the LTE module's AT command interface, to update the LTE module's firmware, and to update the Ethernet sub-system's software.

[Section 9.2](#) describes how to use the LTE Terminal Ethernet link.

### 9.1 Connection Setup

Please complete the following steps to set up the Ethernet connection:

1. Connect the SMA antennas to the LTE Terminal.
2. Connect the power supply, e.g., the mains adapter mentioned in [Chapter 7](#). A green LED indicates the connected power supply, and the LTE Terminal including LTE module is started up. As an alternative it is possible to employ PoE (see [Section 3.7](#)).
3. Plug the Ethernet cable between an external application such as a PC and the LTE Terminal. LTE Terminal as DHCP server is able to provide a valid IP address to the PC.
4. Insert the SIM card. If required to for instance connect to a network provider's Internet gateway, a SIM pin should be entered as described in [Section 9.2.6](#) before inserting the card.
5. The access to the embedded Linux system is only possible via SSH (using e.g., PuTTY for Windows, see following figure for configuration). The default IP address of LTE Terminal is "192.168.1.1"

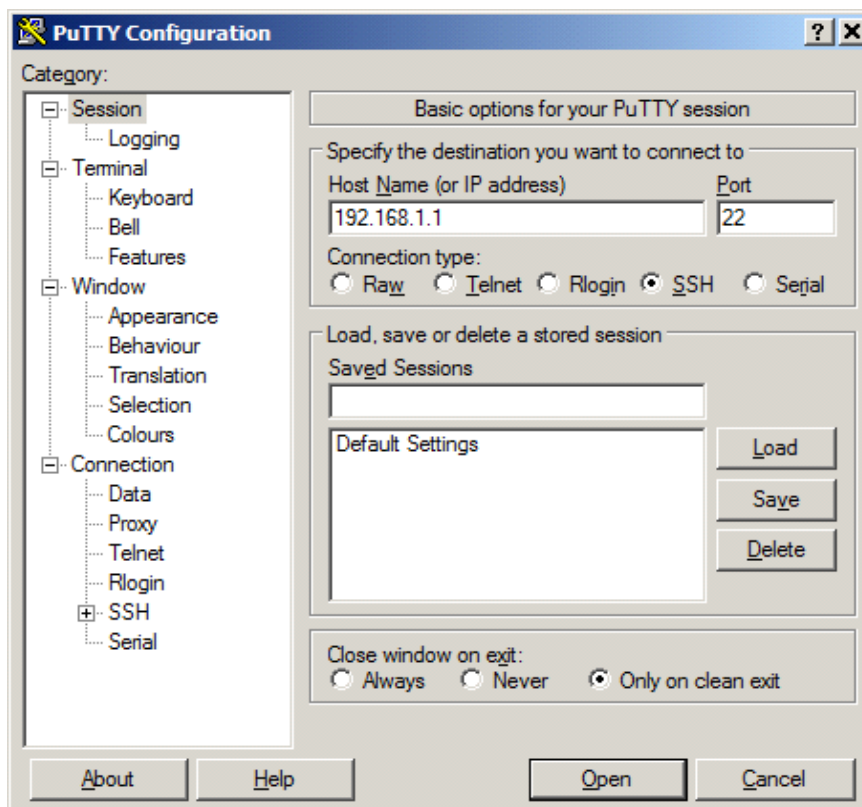


Figure 25: PuTTY Configuration Window

- 6. Open connection in PuTTY and press “Yes” for updating the Key Cache.

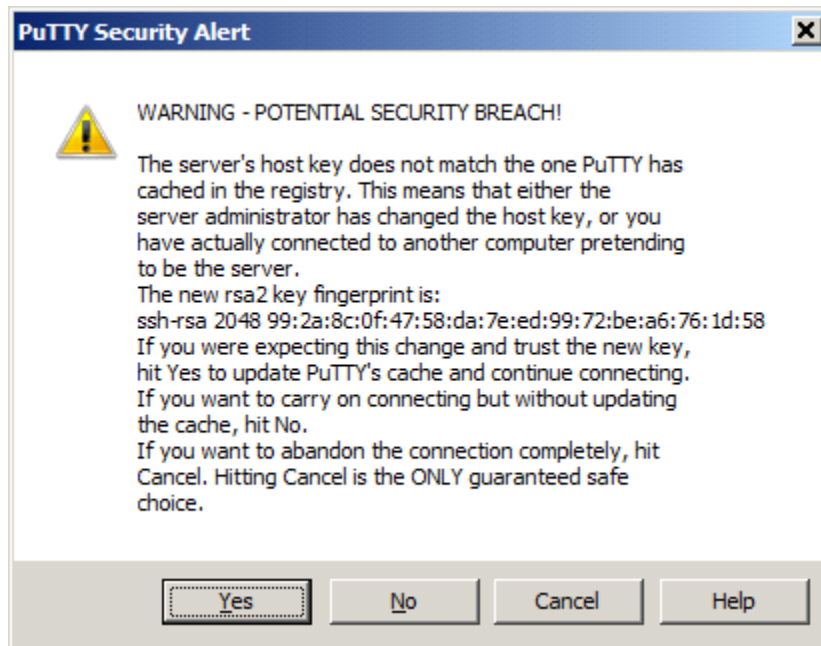


Figure 26: PuTTY Security Alert Window

- 7. Login in as “root” with the initial password given on the packaging label (see red rectangle in Figure 27).

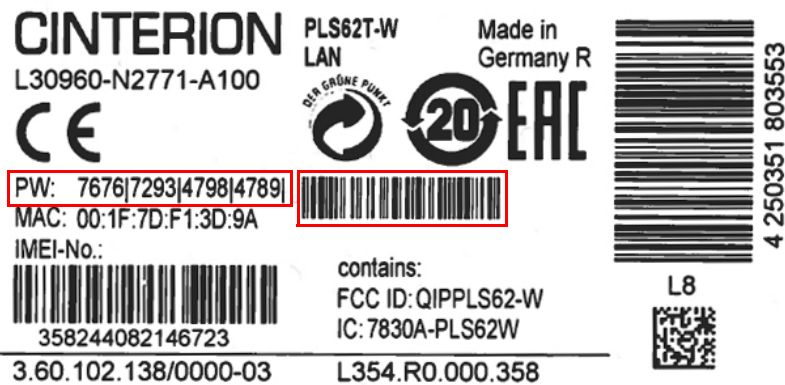


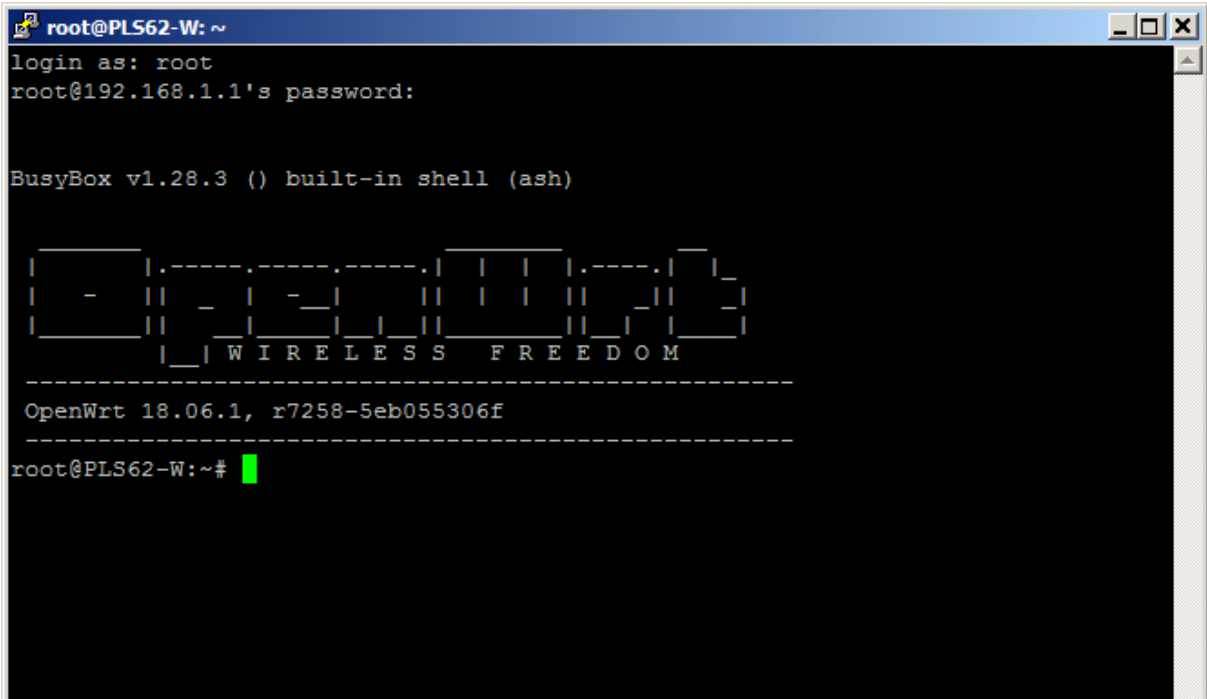
Figure 27: Packaging Label of PLS62T-W LAN



Figure 28: Login via PuTTY

9.1 Connection Setup

8. If the connection setup is successful, the login shell of the LTE Terminal’s embedded Linux system is started, providing root access to the system, and allowing you to for instance change the IP address of LTE Terminal or to edit the default APN list (see [Section 9.2](#) for more information).



**Figure 29:** Logged in to PLS62T-W LAN

9. Having set up the Ethernet connection, the link can now be used to exchange data as well as for administration purposes - see [Section 9.2](#) for details.

A Telnet (or SSH) connection can be terminated by simply closing the PC’s command shell.

**NOTE:** The password can be reseted to the initial password by the procedure described in [Section 9.2.10](#).


## 9.2 Connection Usage

Once the Ethernet connection has been set up successfully, it is possible to transparently exchange data with a remote application, or to administer the LTE terminal. Possible usages are described in more detail in the following sections.

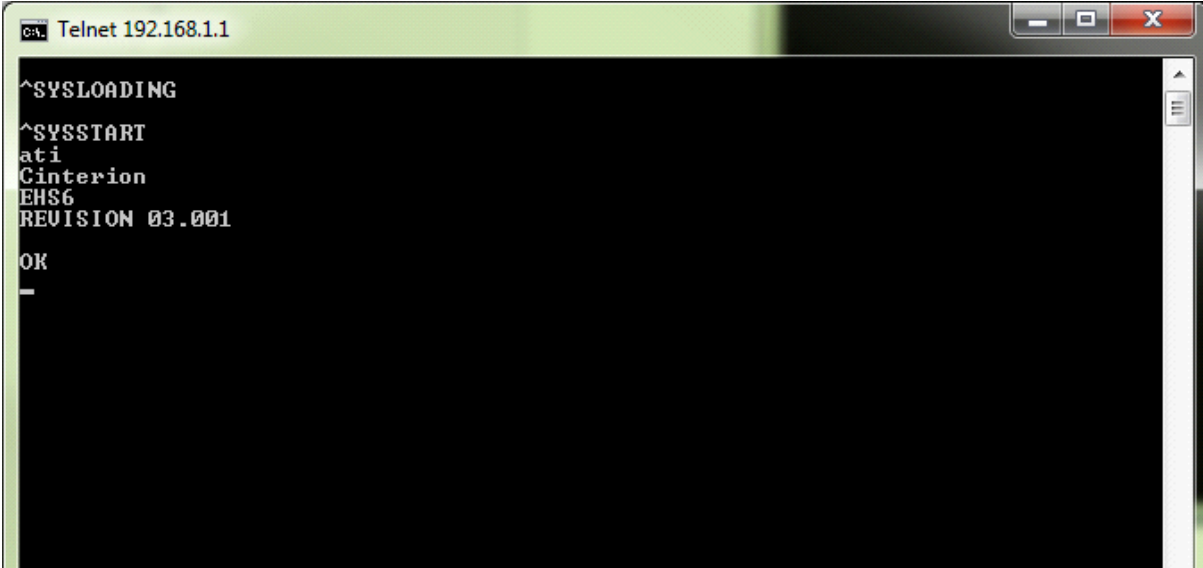
### 9.2.1 Access LTE Terminal via SSH

LTE Terminal can be accessed from a PC via SSH for administrative purposes, and there are two types of connection:

- Calling SSH as described above in [Section 9.1](#) connects to the LTE Terminal's embedded Linux system. The Linux shell provides access to files containing the network configuration (see [Section 9.2.4](#)) as well as the wireless APN configuration (see [Section 9.2.5](#)).
- Calling Telnet with a dedicated port - "1234" - opens a direct link to the LTE module's COM port and with that to the LTE module platform. Over this interface it is possible to control the LTE module, to for instance receive URCs, or query the current firmware version.



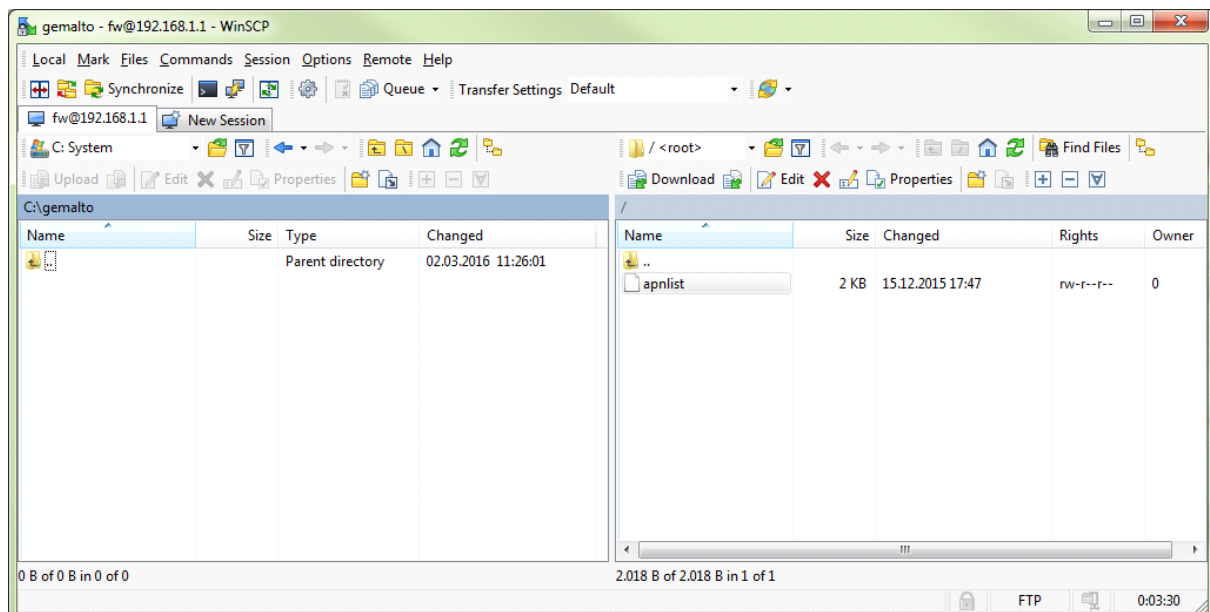
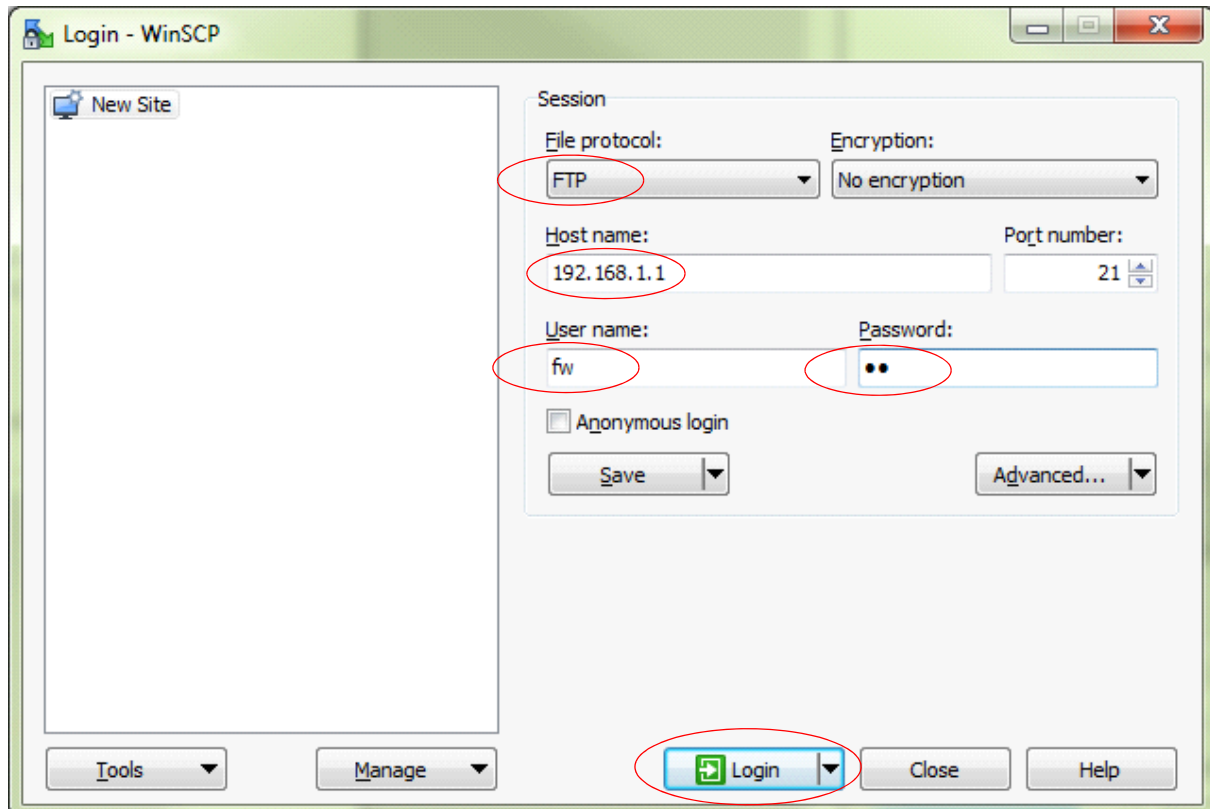
```
C:\Windows\system32\cmd.exe
C:\>telnet 192.168.1.1 1234
```



```
Telnet 192.168.1.1
^SYSLOADING
^SYSSTART
ati
Cinterion
EHS6
REVISION 03.001
OK
-
```

### 9.2.2 Access LTE Terminal via FTP

In addition to Telnet, it is also possible to access the LTE Terminal via FTP, i.e., build a connection to an FTP server on the LTE Terminal that gives access to a defined directory for FTP services. The FTP server's root directory in the embedded Linux file system is "/tmp/fw", the default user name to access FTP services is "fw", and default password is also "fw". If accessing the LTE Terminal's FTP server from a Windows PC, the FTP client should support passive FTP (PASV). The default Windows FTP client does not support passive FTP. However, the FTP client from WinSCP for instance supports this feature. The following figure shows how to use WinSCP to link to the LTE Terminal's FTP server - allowing access to the file "apnlist".





### 9.2.3 Security Notes

Access to the embedded Linux system is only possible via SSH root login (using e.g., PuTTY for Windows).

### 9.2.4 Configure IP Address

To change the default IP address for LTE Terminal, it is necessary to access LTE Terminal's embedded Linux system as root user. This connection can be accomplished by Telnet (or SSH) as described in [Section 9.1](#).

Once connected, the IP address may be configured as follows:

- Open the file `/etc/config/network`. This file can be opened either on the embedded Linux system by using the vi editor, or on an external PC by copying the file to the FTP directory, transferring the file from the FTP directory to the PC, changing it using a text editor, transferring it back to the FTP directory and finally from there to its original directory. For more information on FTP access see [Section 9.2.2](#).
- Edit the file and adapt the line `option ipaddr 192.168.1.1` to for instance `option ipaddr 192.168.2.1`.
- Commit the new IP address by entering `/etc/init.d/network restart`. After the restart, the system can be reached via the new IP address.

### 9.2.5 Edit APN List

Using the APN list, LTE Terminal determines the applicable APN for the inserted SIM card. The APN list matches the unique mobile country and mobile network codes read from the SIM card to the available APNs. By default, the APN list already comprises a comprehensive number of the most commonly used APNs.

However, it may become necessary to add a particular APN, or to remove unnecessary APNs. To do so, please complete the following steps:

- Open the file `/etc/apnlist`. This file can be opened either on the embedded Linux system by using the vi editor, or on an external PC by copying the file to an FTP directory, transferring the file from the FTP directory to the PC, changing it using a text editor, transferring it back to the FTP directory and finally from there to its original directory. For more information on FTP access see [Section 9.2.2](#).  
The file `apnlist` contains one APN per line using the syntax:  
`<3-digit MCC><2-digit MNC> <access point name>`
- Edit the file by adding, changing or removing APNs.
- Save the file. The adapted APN list is available as soon as it is saved.

### 9.2.6 Enter SIM Pin

Normally, LTE Terminal requires a SIM pin before being able to automatically connect to the APN (Internet). **Note:** The SIM pin should be made available permanently to LTE Terminal be-



## 9.2 Connection Usage

fore the SIM is actually required, i.e., usually before the SIM is inserted, and a wireless connection is built up. Also, remember to renew the SIM pin before inserting another SIM card.

Please complete the following steps to set the pin for the SIM card to be inserted:

- Connect to LTE Terminal using Telnet (or SSH). For more information see [Section 9.2.1](#).
- Change to directory “/etc” and call the command to enter the <pin code>:  
root@OpenWrt:/etc# uci set network.3gwan.pincode='<pin code>'
- Save the SIM pin to the flash file system, and activate the pin it through a reboot:  
root@OpenWrt:/etc# uci commit  
root@OpenWrt:/etc# reboot

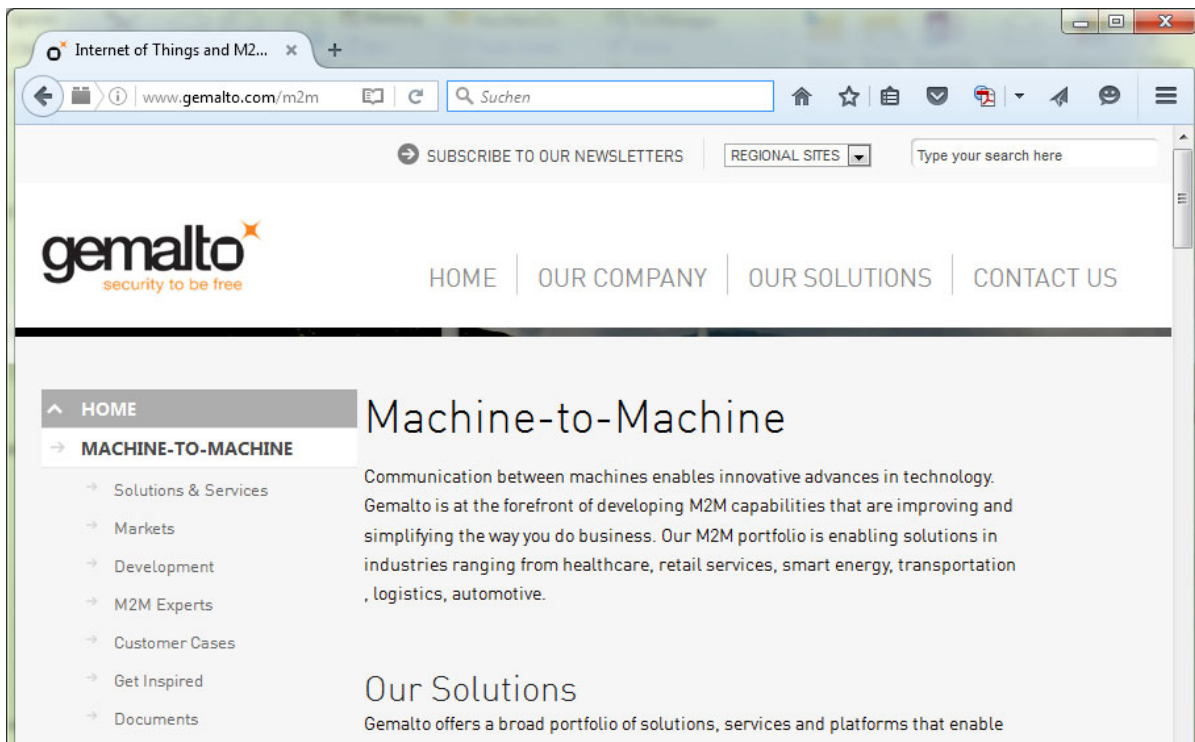
To read the currently used SIM pin please enter the command:

```
root@OpenWrt:/etc# uci show network.3gwan.pincode
```

## 9.2.7 Transparent Data Exchange

To transparently exchange data please complete the following steps:

1. Set up an Ethernet connection as described in [Section 9.1](#), and enter a SIM pin as described in [Section 9.2.6](#).
2. Start an Internet browser on the connected device (e.g., a PC) and call a URL. The PC must be connected to LTE Terminal via Ethernet, and needs to be set up in the same IP sub-network, or has to be able to receive a valid IP address from LTE Terminal’s DHCP server. By means of LTE Terminal, it is now possible to open up an IP connection to a remote host using a wireless network provider’s data gateway.
3. If the Website does not open, the correct APN may not be available in the APN list. The appropriate APN should then be added to the list as described in [Section 9.2.5](#).



### 9.2.8 Update LTE Module Firmware

To update the firmware of the Terminal's LTE module, please complete the following steps:

- Connect to the LTE Terminal using FTP as described in [Section 9.2.2](#).
- Transfer the firmware download files from the PC to the LTE Terminal's FTP directory. The firmware update files have the extensions `*.usf`, `*.jad`, and `*.jar`.
- Connect to the LTE Terminal using Telnet (or SSH) as described in [Section 9.2.1](#).
- Call the firmware update shell script: `./etc/gmup.sh`. After a successful update the LTE terminal restarts automatically.

### 9.2.9 Update Ethernet Sub-System Firmware

To update the software of the LTE Terminal's Ethernet sub-system, please complete the following steps:

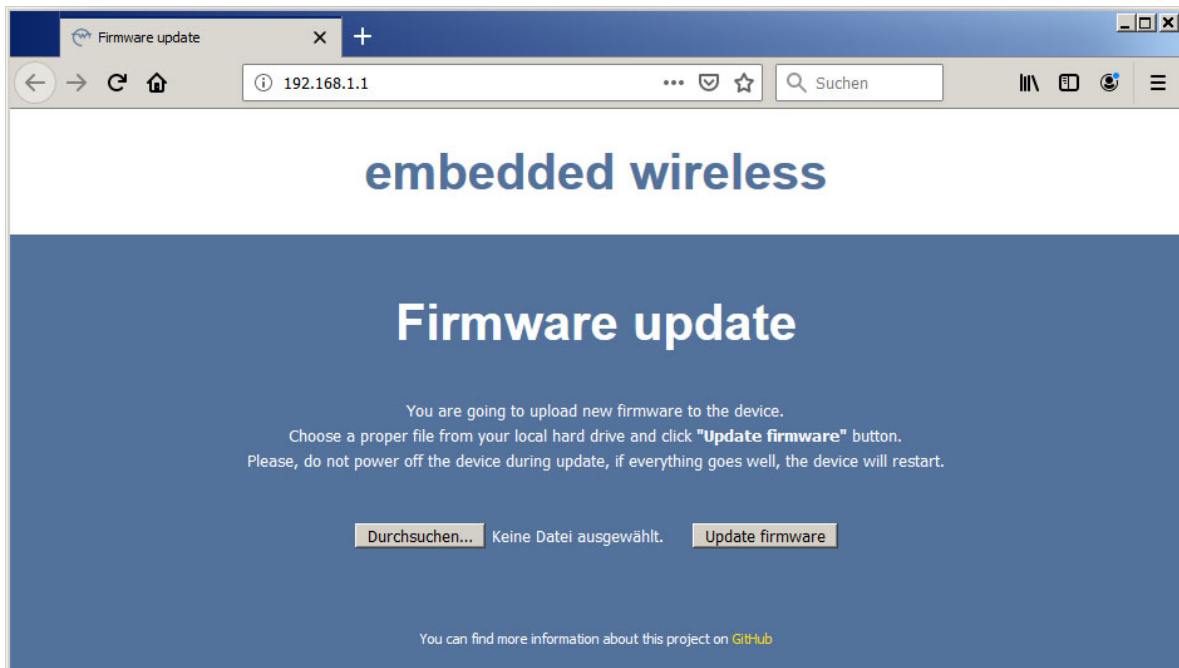
1. Download the Firmware Update file (e.g. "openwrt-18.06.1-gto-ew-r25-8bbd8e3.bin") from the Thales server to your PC
2. Connect to the LTE Terminal using FTP as described in [Section 9.2.2](#).
3. Transfer the software download file from the PC to the LTE Terminal's FTP directory. The software update file has the extension `*.bin`, e.g., "openwrt-18.06.1-gto-ew-r25-8bbd8e3.bin".
4. Connect to the LTE Terminal using SSH as described in [Section 9.2.1](#).
5. Call the software update shell script: `./etc/ewup.sh`. After a successful update the LTE Terminal restarts automatically.

### 9.2.10 Update Ethernet Sub-System Firmware without Password

In case that the own password got lost, there is a possibility to reset the password to the initial password, which is printed on the package label (see [Figure 27](#)). During the procedure, the same or a newer SW version of Ethernet Sub-System has to be installed.

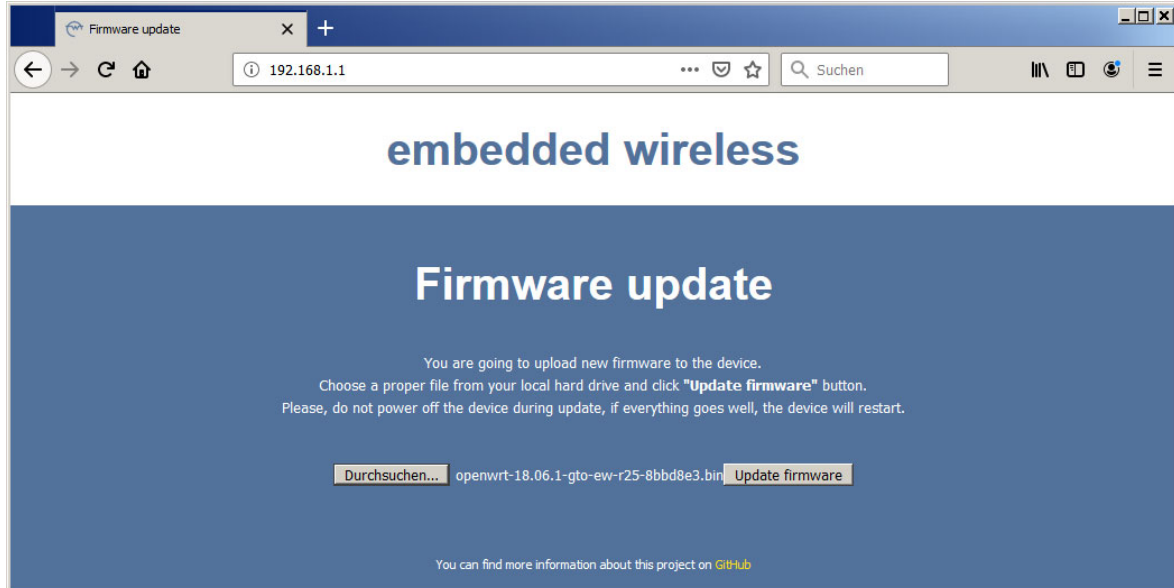
Procedure:

1. Download the Firmware Update file (e.g. "openwrt-18.06.1-gto-ew-r25-8bbd8e3.bin") from the Thales server to your PC
2. Switch off terminal PLS62T-W LAN
3. connect PC with the Firmware Update file to the LAN-Interface of the terminal PLS62T-W LAN
4. Power-On the terminal PLS62T-W LAN with RST\_IN signal active for 3 seconds (Power Connector see [Section 3.9](#)).
5. Wait until IP Address and Gateway Address are assigned to the PC. The assignment can be checked by the command `ipconfig` in a DOS-Box.
6. Open Internet Browser Window to the Gateway Address assigned by the terminal PLS62T-W LAN and the Firmware Update site is shown (see [Figure 30](#)).



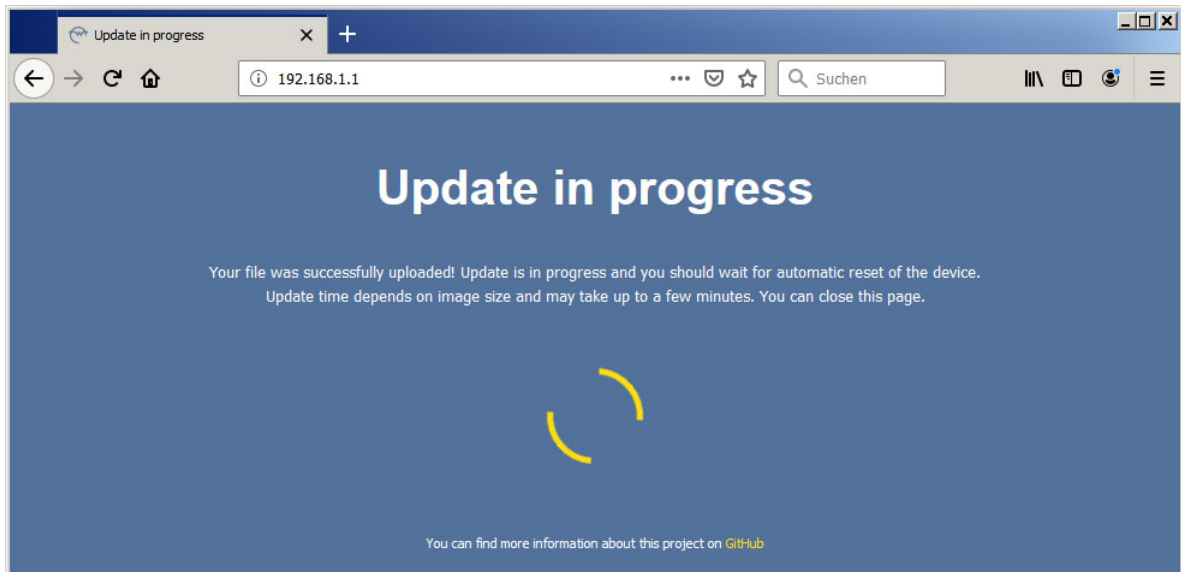
**Figure 30:** Firmware Update Window

7. Click on “Durchsuchen...” to select the SW Update file, which was downloaded before and click then on “OK”.



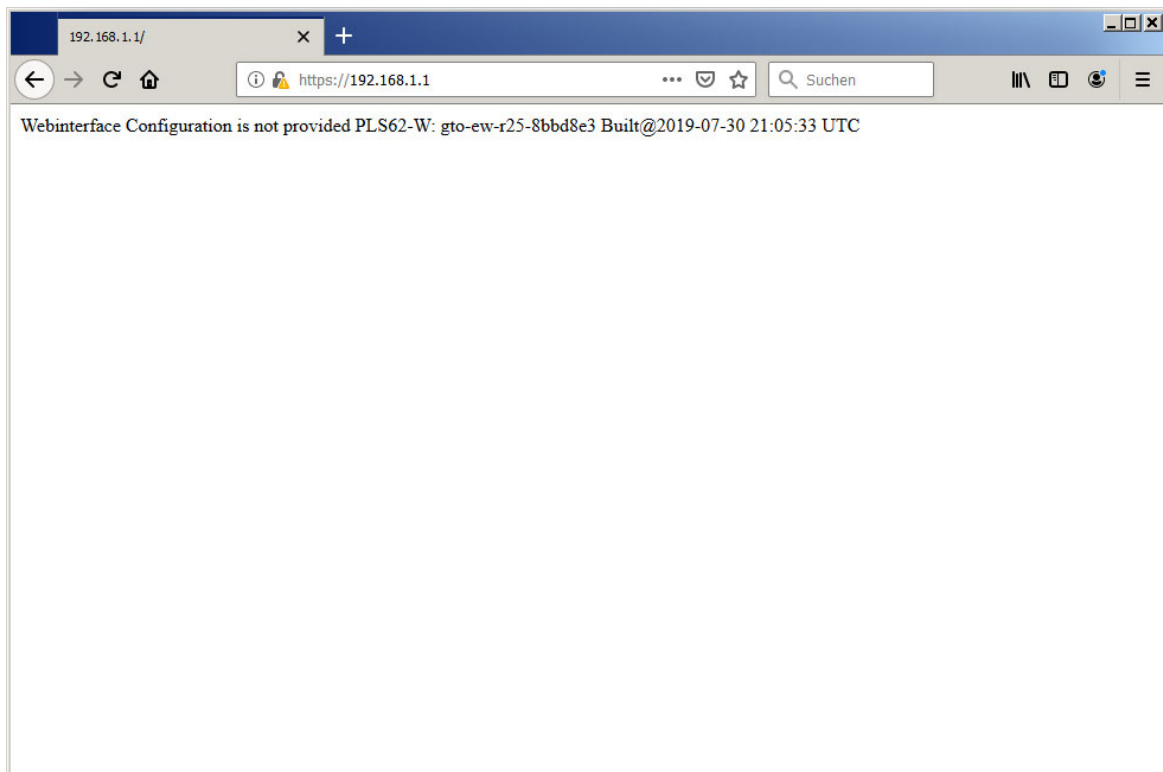
**Figure 31:** Firmware for update selected

8. Click on “Update Firmware” and the firmware will be downloaded to the terminal and updated. Wait until the update is done, don't switch off the terminal nor unplug the power supply.



**Figure 32:** Firmware Update in progress

- Update is finished and the password is reseted to the initial password when the following screen appears (see [Figure 33](#)).



**Figure 33:** Firmware update done



**THALES DIS AIS Deutschland GmbH**  
Werinherstrasse 81  
81541 Munich  
Germany

**THALES**